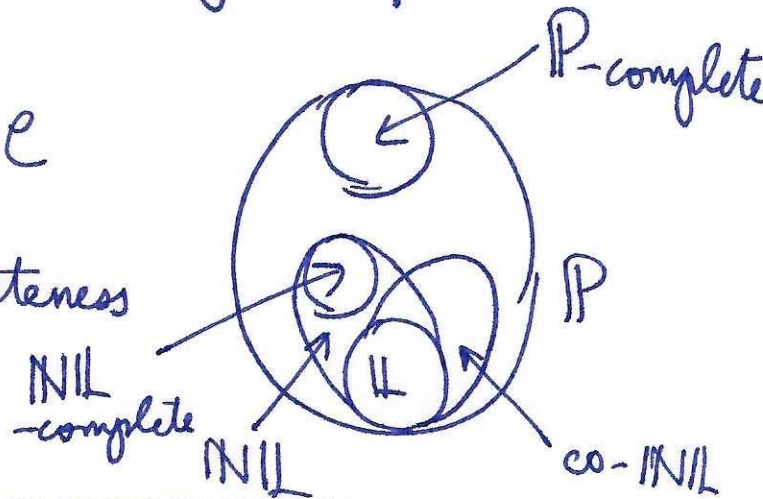


Def: A language L is NL -complete if

- (1) $L \in NL$
- (2) $\forall B \in NL \quad B \leq_L L$

(We can also define P -completeness using logspace reductions.)



$$L \leq_p L' \quad L' \leq_p L'' \Rightarrow L \leq_p L'' \quad (\text{closure of polynomials})$$

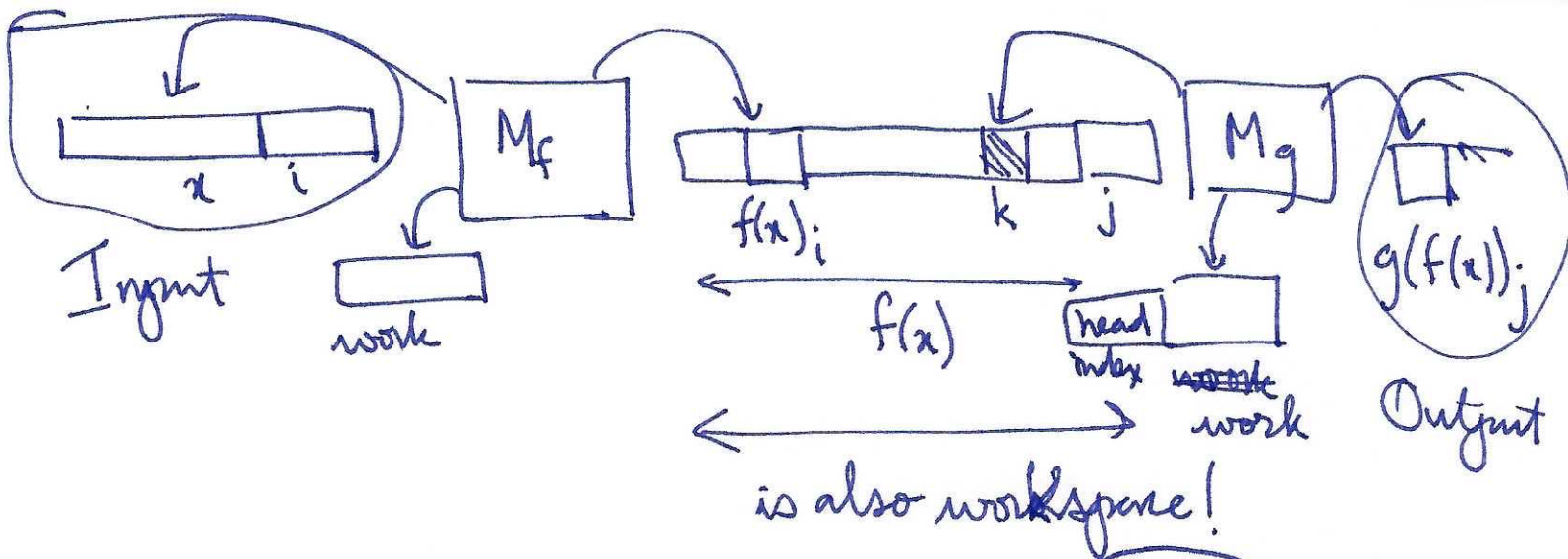
Thm: $L \leq_L L', L' \leq_L L'' \Rightarrow L \leq_L L''$ Logspace reductions can be chained.

Proof: Let f reduce L to L' and g reduce L' to L''

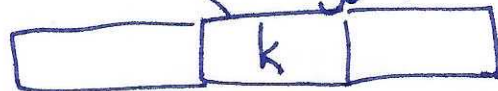
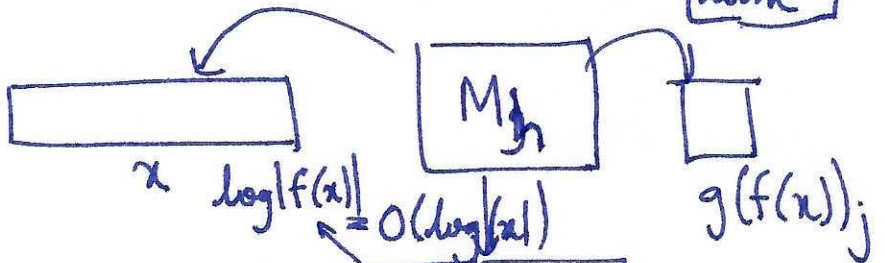
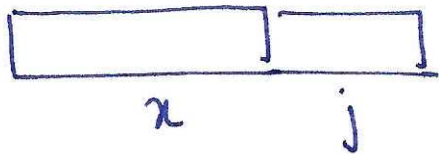
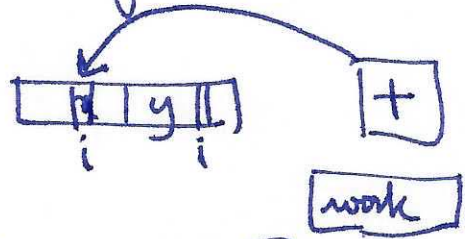
f is implicitly logspace computable by machine M_f
 g " " " " " " " " M_g .

We want to construct reduction h that is imp. log. comp.
 $h(x) = g(f(x))$ (from L to L'')

(Failed proof) ~~Use M~~ Let's start with x . $\rightarrow |f(x)|$ bits polynomial!
 M_f can write down $f(x)$ in logspace, by taking input $\langle x, i \rangle$ (for all $i \leq |x|^c$).
 M_g can write down the j^{th} bit of $g(f(x))$ using M_g .



We want j 'th bit of $g(f(x))$



At any stage, k has some value and M_h is simulating M_g .

workspace index workspace
of M_f of "head" of M_g $\log|f(x)| = O(\log|x|)$
 $\log|x|$ $f(x)$ that "head" reads $= O(\log|x|)$

- (1) M_h runs M_f on $\langle x, k \rangle$ to get $f(x)_k$
- (2) Based on $f(x)_k$ and state of M_g (and workspace of M_g), determine next state of M_g and whether head (of M_g 's input) moves \leftarrow / \rightarrow .
- (3) If \leftarrow , decrement k , else increment.

This is a logspace simulation of M_g on $f(x)$.
 k can be stored in logspace.



$\text{DIRPATH} = \{ \langle G, s, t \rangle \mid G \text{ is a directed graph, } s, t \text{ are vertices and } \exists \text{ path from } s \text{ to } t \}$
 PATH

Thm: PATH is NL-complete.

Proof: (1) Prove that $\text{PATH} \in \text{NL}$.

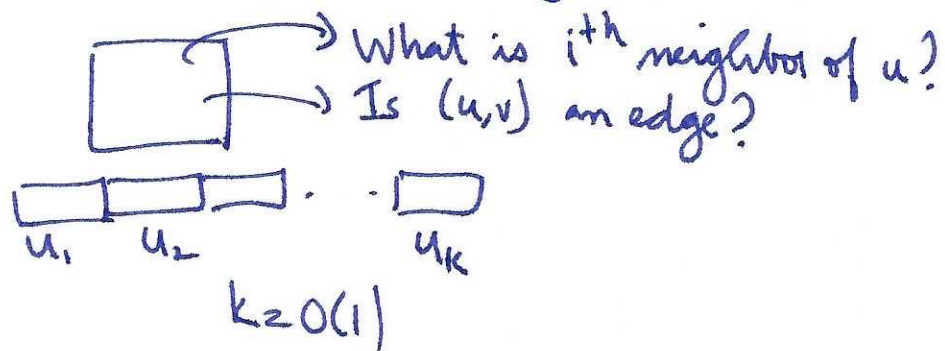
What is a logspace machine in the context of graph algorithms?

Imagine G is adjacency matrix (n vertices).

In $O(\log n)$ space, we can store a vertex.

Meaning, a logspace machine can only store $O(1)$ vertices (vertex labels).

It can look up neighbors (using input and counting)



$\text{PATH} \in \text{NL}$. Because machine can non-deterministically guess the next vertex in path from s to t .

(2) To prove $\forall E \in NL, E \leq_L PATH$

Let M be an NTM machine deciding E .

M uses $\leq \boxed{c \log n}$ space (c is constant).

M accepts (input) x iff in config. graph $G_{M,x}$
 C_{start} can reach C_{Acc}

M accepts x iff $\langle G_{M,x}, C_{start}, C_{Acc} \rangle \in PATH$

Need to argue that given x $\langle \underline{G_{M,x}}, \underline{C_{start}}, \underline{C_{Acc}} \rangle$
can be implicitly computed in logspace.

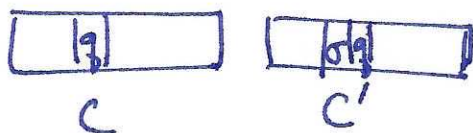
Compute:

1) i^{th} bit of C_{Acc} : easy (doesn't depend on x) $C_{Acc} = q_{Acc} \llllll$

2) i^{th} bit of C_{start} : $C_{start} = q_0 \boxed{x}$ i^{th} bit of C_{start}
fixed $i - \Theta(1)$ bit of x

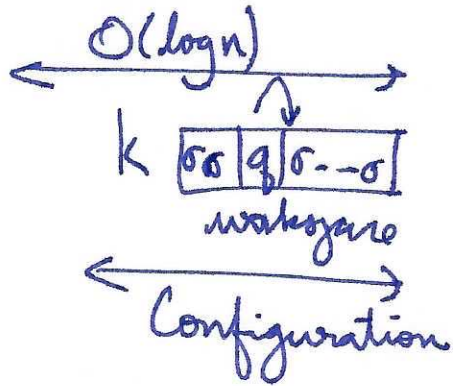
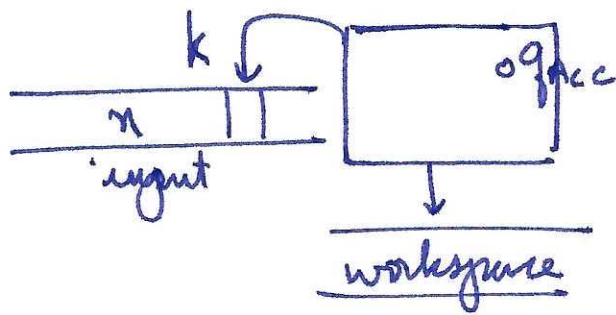
3) $(i,j)^{th}$ bit of ~~C_{start}~~ $G_{M,x}$: C, C' are config. $|C|, |C'| = O(\log n)$
 $(C, C')^{th}$ bit

Can we determine if C' follows C (acc. to
NTM transitions of M) in logspace? Yes

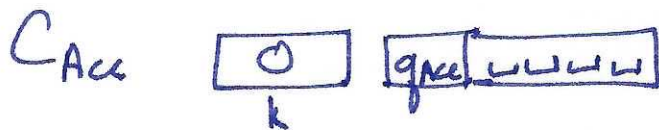
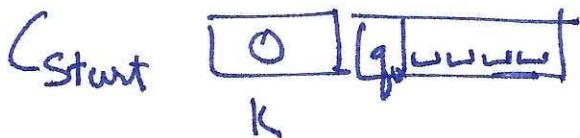


Check that head moves
correctly, and all other
symbols are unchanged.
(not touched by head)





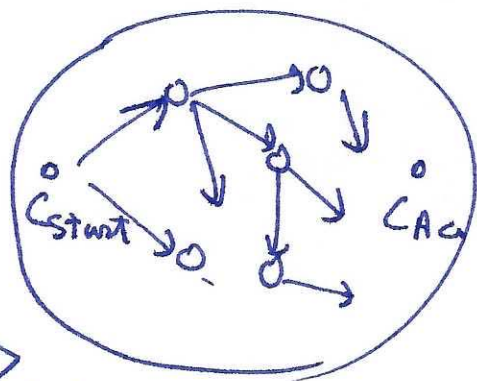
$L \in NL$ M logspace NTM deciding L
 $x \in L$? Write out all config of M (fixing x)
 At most $\text{poly}(n)$. There are $\text{poly}(n)$ vertices in $G_{M,x}$



$x \in L$

iff

$\langle G_{M,x}, C_{start}, C_{acc} \rangle \in \text{PATH}$



$f(x) = \langle G_{M,x}, C_{start}, C_{acc} \rangle$

PATH is INIL-complete.

$\text{UPATH} = \{ \langle G, s, t \rangle \mid G \text{ is a undirected graph and } s \text{ is connected to } t \}$
 UST con

[Reinagold 05] $\text{UPATH} \in \Pi$.