## A Lattice-Based Approach to Deterministic Parallelism

Lindsey Kuper and Ryan R. Newton Indiana University

MPI-SWS
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## What does this program evaluate to?

$$
\text { let }{ }_{-}=\text {put } l 0 \text { in }
$$

$$
\text { let par } v=\text { get } l
$$

$$
{ }_{-}=\operatorname{put} l 8
$$

in $v$

## Disallow multiple writes?

let ${ }_{-}$put $l 0$ in let par $v=$ get $l$
_ $=$ put $l 8$
in $v$

## Disallow multiple writes?

## let ${ }_{\text {_ out } l 0}$ in let par $v=$ get $l$


in $v$

Tesler and Enea, 1968
Arvind et al., 1989
"IVars"

Deterministic programs that single-assignment forbids
let ${ }_{-}=$put $l 3$ in
let par $v=$ get $l$
_ = put $l 3$
in $v$

## Deterministic programs that single-assignment forbids

let ${ }_{-}=$put $l 3$ in
let par $v=$ get $l$
_ = put $l 3$
in $v$
let par _ $=$ put $l(4, \perp)$
${ }_{-}=$put $l(\perp, 3)$
in get $l$

## Deterministic programs that single-assignment forbids

$$
\text { let }{ }_{-}=\text {put } l 3 \text { in }
$$

$$
\text { let par } v=\text { get } l
$$

$$
{ }_{-}=\text {put } l 3
$$

in $v$

$$
\begin{aligned}
\text { let par } & =\text { put } l(4, \perp) \\
- & =\text { put } l(\perp, 3)
\end{aligned}
$$

in get $l$


## From Concurrent Collections...

## Concurrent Collections

 Vivek Sarkar ${ }^{1}$ Frank Schlimbach ${ }^{2}$ Sağnak Taşrlar $^{1}$
${ }^{1}$ Rice University ${ }^{2}$ Intel Corporation ${ }^{3}$ UCLA



1 Introduction
With multioore processors, parallel computing is going mainstream. Yet most
software is still written in traditional serial languages with explicit threading Sotware is still written in traditional serial languages with explicit threading High-level parallel programming models, atter four decades of proposals, have
still not seen widespread adoption. This is beginning to change. Systems like MapReduce are succeeding based on implicit parallelism. Other systems like Nvidia CUDA are partway there, providing a restricted programming model to the user but also exposing too many of the hardware details. The payoff for
high-level programming model is clear-it can provide semantic guarantees and can simplify the understanding, debugging, and testing of a parallel program. In this paper we introduce the Concurrent Collections ( CnC C) programming
model, built on past work on TStreams $[13$. CnC falls into the same family model, bliut an past work on TItreams 13 . CnC falls into the same family communicating with one another. In CnC, those computations are called ste ${ }^{2}$ ? and are related by control and data dependences. CnC is provably determin
stic. This limits CnC 's scope, but compared to its more narrow counterpart istic. This imits Cnc s scope, but compared to its more narrow counterparts
StreamIT, NP-Click, etc), $\mathrm{C} C$
is suited for many applications - incorpoparating static and dynamic forms of task, data, loop, pipeline, and tree parallelism.
Truly mainstream parallelism will require reaching the large community of Truly mainstream parallelism will require reaching the large community
non-professional programmers scientist, animators, and financial analysts
but reaching them requires a separation of concerns between application log non-protessional programmers scientists, animators, and financial analysts
but reaching them reuures a separation of concors between application ologic
and parallel implementation. We say that the former is the concern of the and parallel implementation. We say that the former is the concern of the
domain ezpert and the latter of the performance tuning expert. The tuning domain erpert and the latter of the performanoe tuning expert. The tuning
expert is given the maximum possible freedom to map the computation onto the target architecture and is not required to have an understanding of the domain
target architecture and is not required to have an understanding of the domain

Budimlić et al., 2010

## From Concurrent Collections...

Concurrent Collections


${ }^{1}$ Rice University ${ }^{2}$ Intel Corporation ${ }^{3}$ UCLA
Abstract
We introduce the Concurrent Collections $(\mathrm{CnC})$ programming model
$\mathrm{CnC}^{\text {s. supparts }}$ lexible combinations of task and data parallelism while
 ing high-kevel operations al
together
Wofm $\mathrm{a} C \mathrm{Ccg}$ gruph.
We formally describe the execution semantics of CnC and prove
that the model guarantees deeterministic computation. We evaluate the
that the model g guaranteres deererministic computation. We cealuate the
performance of CnC implementations on several applications and show
that $C$ an perrion CnC ofers perfornance and scalability equivalent to or beter than
that
that offered dy lown Introduction
with multicore processors, parallel computing is going mainstream. Yet most High-level peralel programming models, after four decades efp propit threading still not seen widespread adoption. This is beginning to change. Systems like MapReduce are succeeding hoend on imolicit parallelism oth- ovetoms like Nvidia CUDA are par
the user but also expr high-level programmi can simplify the under In this paper we if
hodel, built on past model, built on past
as dataflow and strea as dataflow and strea and are related by ef tic. This limits Cn
StreamIT, NP-Click StreamIT, NP-Click Truly mainstream
mon-professional prog non-professional prop
put reaching them r but reaching them
and parallel imple domain ezpert and ,
expert is given the n target architecture a A strength of CnC

The key language feature that enables determinism is the single assignment condition. The single assignment condition guarantees monotonicity of the data collection $A$. We view $A$ as a partial function from integers to integers and the single assignment condition guarantees that we can establish an ordering based on the non-decreasing domain of $A$.

Budimlić et al., 2010

## Monotonicity

$f$ is monotonic iff, for a given $\leq$,

$$
x \leq y \Longrightarrow f(x) \leq f(y)
$$



THE SEMANTICS OF A SIMPLE LANGUAGE FOR PARALLEL PROGRAMMING
Gilles KAHN
IRIA-Laboria, Domaine de Voluceau, 78150
Rocquencourt, France
and
Commissariat à l'Energie Atomique, France
In this paper, we describe a simple language for paraliel programming. Its semantics 1 s studied thor-
ough1y. The desirable properties of this language and tits deficiencies are exhibited by this theoret-



There is a wide disagreement amons systens designers
as to that are the best primitives for writing sys-
tens as to what are the est primitives for writing sys
tems progzams. In this paper, we describe simp
language for parallel programing and sudy its language for paralle1 progr
mathematical properties.

1. a smple language for parallel programing.
The features of our mini-language are exhibited on
the sample program $s$ on $f i$. 1 . The corventions are

 channel are declared at line (1), and for any simple
type $\sigma$ (boolean, rean , etc....) we could have decla-



 1ines
put
line.
put ine.
The of a process is an usual Algol program except
for invocation of wait on an input 1ine (e.g. at (4)) or send a variable on a 1 ine of compatitle type
(e.8. at ( 5 )). The process stays blocked on a wait (e.e. at (S)). The process stays blocked on a wait
nntil something is being sent on this 1 ine by ano nit sone thing is being sent on this line by ano
her process, but nothing can prevent $a$ process from performing a send on an ine
In other words, processes comuni irst-out (fifo) queues.
Calling instances of the
Calling instances of the processes is done in the body of the main prograum at line (ine (6) where the the
cctual names of the chane actual names of the channels are bound to the formal
parameters of the processes. The inf ix operator par
initiates the con parane ters of the processes. The infix operator par
initiates the concuren activation of the processes.
Such $a$ syyle of progranning is cilose to may systems

 on fig. 2.1 where the nodes represent processes and
the arcs communication channe 1 between these processes.
What sort


 were to stop at some time for an extraneous reason,
the whole system would stop
 of a paralle1 program and to prove them within ${ }^{2}$,
formal logical framework is the central motivation for the theoretical stu
2. parallel computation

Begin

(4) (7) $_{\text {I }}$ in if if then wait (U) else wait (V) print (I) ; in wait(U) else wait(V)





(3) Process ${ }^{\text {End }} \mathrm{h}$ (integer in U integer out $\mathrm{v}_{\mathrm{i}}$,



Carment : body of mainprogram
End ;
Fig.1. Smple parallel program $s$.


## to KPNs

and

Monotonicity means that receiving more iuput at a computing station can only provoke it to send more output. Indeed this a crucial property since it allows parallel operation : a machine need not have all of its input to start computing, since future input concerns only future putput.

Monotonicity enables deterministic parallelism!

## Parameterizing our language: LVars




Counter

## Parameterizing our language: LVars



## Parameterizing our language: LVars



Pair of IVars

## Parameterizing our language: LVars



## Parameterizing our language: LVars

let ${ }_{-}=$put $p\{(\perp, 4)\}$ in let par $v_{1}=$ getFst $p$

$$
-\quad=\operatorname{put} p\{(3,4)\}
$$ in $\ldots v_{1} \ldots$



Pair of IVars

## Parameterizing our language: LVars

let $\quad=$ put $p\{(\perp, 4)\}$ in let par $v_{1}=$ getFst $p$

$$
-\quad=\operatorname{put} p\{(3,4)\}
$$ in $\ldots v_{1} \ldots$


getFst $p \triangleq$ get $p\{(n, \perp) \mid n \in \mathbb{N}\}$


## Two take-aways

Monotonicity enables deterministic parallelism

Monotonically increasing writes

+ threshold reads
= deterministic parallelism


## Determinism for $\lambda_{\mathrm{LVar}}$

## Determinism



## Determinism for $\lambda_{\mathrm{LVar}}$



Determinism


## Determinism for $\lambda_{\mathrm{LVar}}$


"Independence"
Determinism


## Why we need Independence

To show: There exists $\sigma_{c}$ such that

## Why we need Independence

By induction hypothesis, there exist $\sigma_{c_{1}}, \sigma_{c_{2}}$ such that

$$
\left\langle S_{a_{1}} ; e_{a_{1}}^{\left\langle S ; e_{1}\right\rangle}\left\langle S_{b_{1}}^{\langle } ; e_{b_{1}}\right\rangle \quad\left\langle S_{a_{2}} ; e_{\left.a_{a_{2}}\right\rangle}^{\left\langle S ; e_{2}\right\rangle}\right.\right.
$$

To show: There exists $\sigma_{c}$ such that

$$
\langle S_{a_{1}} \cup_{S} S_{a_{2}} ; \underbrace{\left\langle e_{a_{1}} e_{a_{2}}\right\rangle}_{\sigma_{c}} \overbrace{\left\langle e_{1} e_{2}\right\rangle}^{\left\langle S_{b_{1}} \cup_{S} S_{b_{2}}\right.} ; e_{b_{1}} e_{\left.b_{2}\right\rangle}\rangle
$$

## Determinism for $\lambda_{\mathrm{LVar}}$



Independence

$$
\frac{\langle S ; e\rangle \hookrightarrow\left\langle S^{\prime} ; e^{\prime}\right\rangle}{\left\langle S \sqcup_{S} S^{\prime \prime} ; e\right\rangle \hookrightarrow\left\langle S^{\prime} \sqcup_{S} S^{\prime \prime} ; e^{\prime}\right\rangle}
$$



## Independence

Independence

$$
\begin{aligned}
\langle S ; e\rangle & \longleftrightarrow\left\langle S^{\prime} ; e^{\prime}\right\rangle \\
\left\langle S \sqcup_{S} S^{\prime \prime} ; e\right\rangle & \longleftrightarrow\left\langle S^{\prime} \sqcup_{S} S^{\prime \prime} ; e^{\prime}\right\rangle
\end{aligned}
$$

## Independence

# "That looks kind of like a frame rule." —Amal, March 2012 

Independence

$$
\begin{aligned}
\langle S ; e\rangle & \longleftrightarrow\left\langle S^{\prime} ; e^{\prime}\right\rangle \\
\left\langle S \sqcup_{S} S^{\prime \prime} ; e\right\rangle & \longleftrightarrow\left\langle S^{\prime} \sqcup_{S} S^{\prime \prime} ; e^{\prime}\right\rangle
\end{aligned}
$$

## Independence

## Frame

$$
\frac{\{p\} c\{q\}}{\{p * r\} c\{q * r\}}
$$

Independence

$$
\frac{\langle S ; e\rangle}{\left\langle S \sqcup_{S} S^{\prime \prime} ; e\right\rangle} \longleftrightarrow\left\langle S^{\prime} ; e^{\prime}\right\rangle
$$

## More in our TR

- Complete syntax and semantics
- Proof of determinism
- Subsuming existing models
- KPNs, monad-par
- Support for controlled nondeterminism
- "probation" state



## Thanks!

Email: Ikuper@cs.indiana.edu Twitter: @lindsey
Web: cs.indiana.edu/~Ikuper Research group:
lambda.soic.indiana.edu


- LATTICE-BASED DETERMINISTIC PARALLELISM

