Freeze After Writing:

Quasi-Deterministic
Parallel Programming with LVars

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POPL '14, January 23, 2014



Freeze After Writing

- **LVars**
- Quasi-Deterministic
 Parallel Programming



```
data Item = Book | Shoes | ...
deriving (Show, Ord, Eq)
```



```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
```



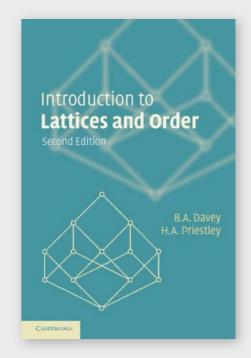
```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty</pre>
```



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```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
```



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```
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  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
        async $ atomicModifyIORef cart
        (\m -> (insert Shoes 1 m, ()))
```



```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
        async $ atomicModifyIORef cart
        (\m -> (insert Shoes 1 m, ()))
        res <- async $ readIORef cart</pre>
```



```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
        async $ atomicModifyIORef cart
            (\m -> (insert Shoes 1 m, ()))
        res <- async $ readIORef cart
        wait res</pre>
```



```
landin:lvar-examples lkuper$ make map-ioref-data-race
 ghc -02 map-ioref-data-race.hs -rtsopts -threaded
 [1 of 1] Compiling Main
                                                                                                                                                                  ( map-ioref-data-race.hs, map-ioref-data-race.o )
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while true; do ./map-ioref-data-race +RTS -N2; done
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```

```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
        async $ atomicModifyIORef cart
            (\m -> (insert Shoes 1 m, ()))
        res <- async $ readIORef cart
        wait res</pre>
```



```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
        async $ atomicModifyIORef cart
        (\m -> (insert Shoes 1 m, ()))
        res <- async $ readIORef cart
        wait res</pre>
```







```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)
p :: IO (Map Item Int)
p = do cart <- newIORef empty</pre>
       a1 <- async $ atomicModifyIORef cart</pre>
                (\m -> (insert Book 1 m, ()))
       a2 <- async $ atomicModifyIORef cart</pre>
                (\m -> (insert Shoes 1 m, ()))
       res <- async $ do waitBoth a1 a2
                           readIORef cart
       wait res
```

Deterministic, but only because we put the waits in the right places

Deterministic, but only because we put the waits in the right places

```
p:: Par Det s (IMap Item s Int)
p = do
    cart <- newEmptyMap
    fork $ insert Book 1 cart
    fork $ insert Shoes 1 cart
    return cart

main = do
    putStr $ show $ toList $
    fromIMap $ runParThenFreeze p</pre>
```

Deterministic by construction

Freeze After Writing

- **LVars**
- Quasi-Deterministic
 Parallel Programming

```
data Item = Book | Shoes | ...
  deriving (Show, Ord, Eq)

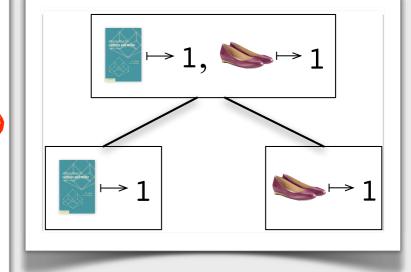
p :: IO (Map Item Int)
p = do cart <- newIORef empty
        async $ atomicModifyIORef cart
        (\m -> (insert Book 1 m, ()))
        async $ atomicModifyIORef cart
            (\m -> (insert Shoes 1 m, ()))
        res <- async $ readIORef cart
        wait res</pre>
```





IVars: single writes, blocking (but exact) reads [Arvind et al., 1989]





IVars: single writes, blocking (but exact) reads [Arvind et al., 1989]

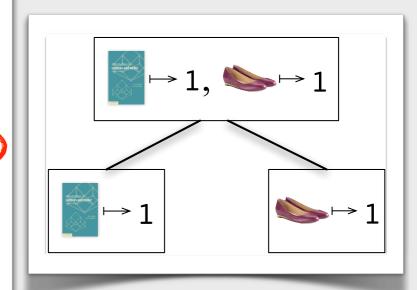


 \mapsto 1, \mapsto 1

IVars: single writes, blocking (but exact) reads [Arvind et al., 1989]

LVars: multiple least-upper-bound writes, blocking threshold reads



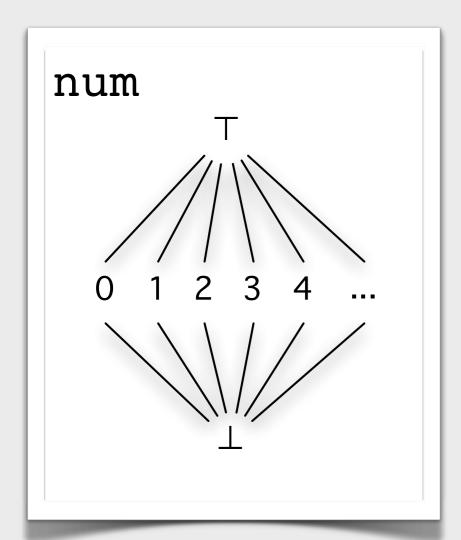


IVars: single writes, blocking (but exact) reads [Arvind et al., 1989]

LVars: multiple least-upper-bound writes, blocking threshold reads



* actually a bounded join-semilattice



```
Raises an error, since 3 \sqcup 4 = T

do

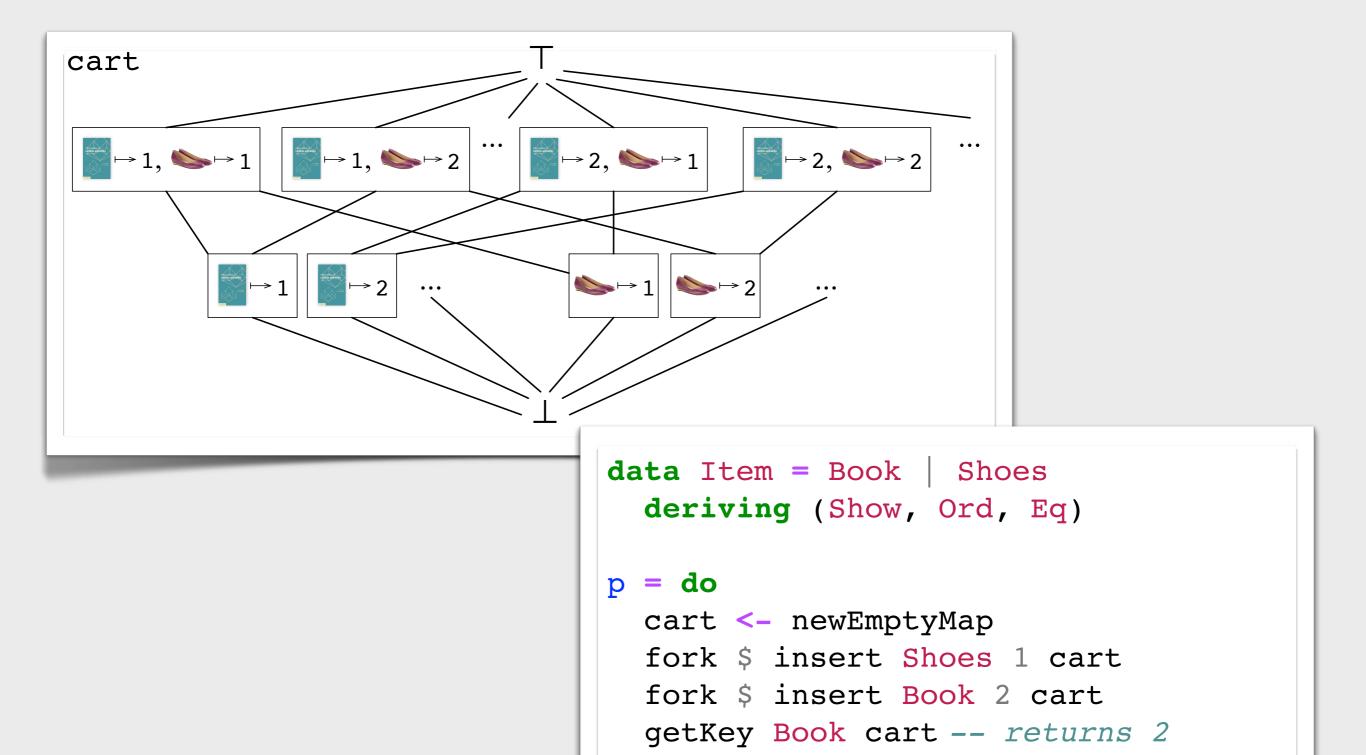
fork (put num 3)
fork (put num 4)
```

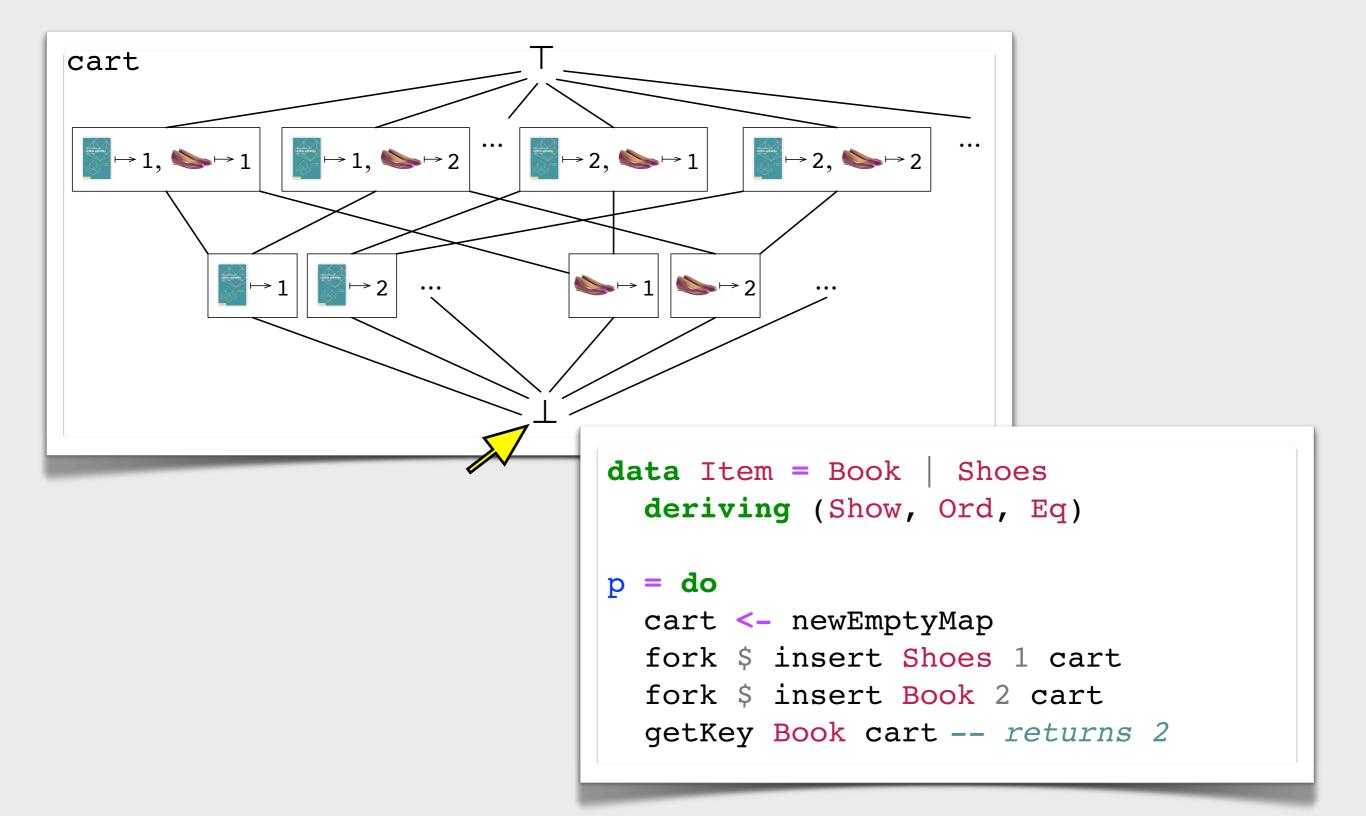
```
Works fine, since 4 \sqcup 4 = 4

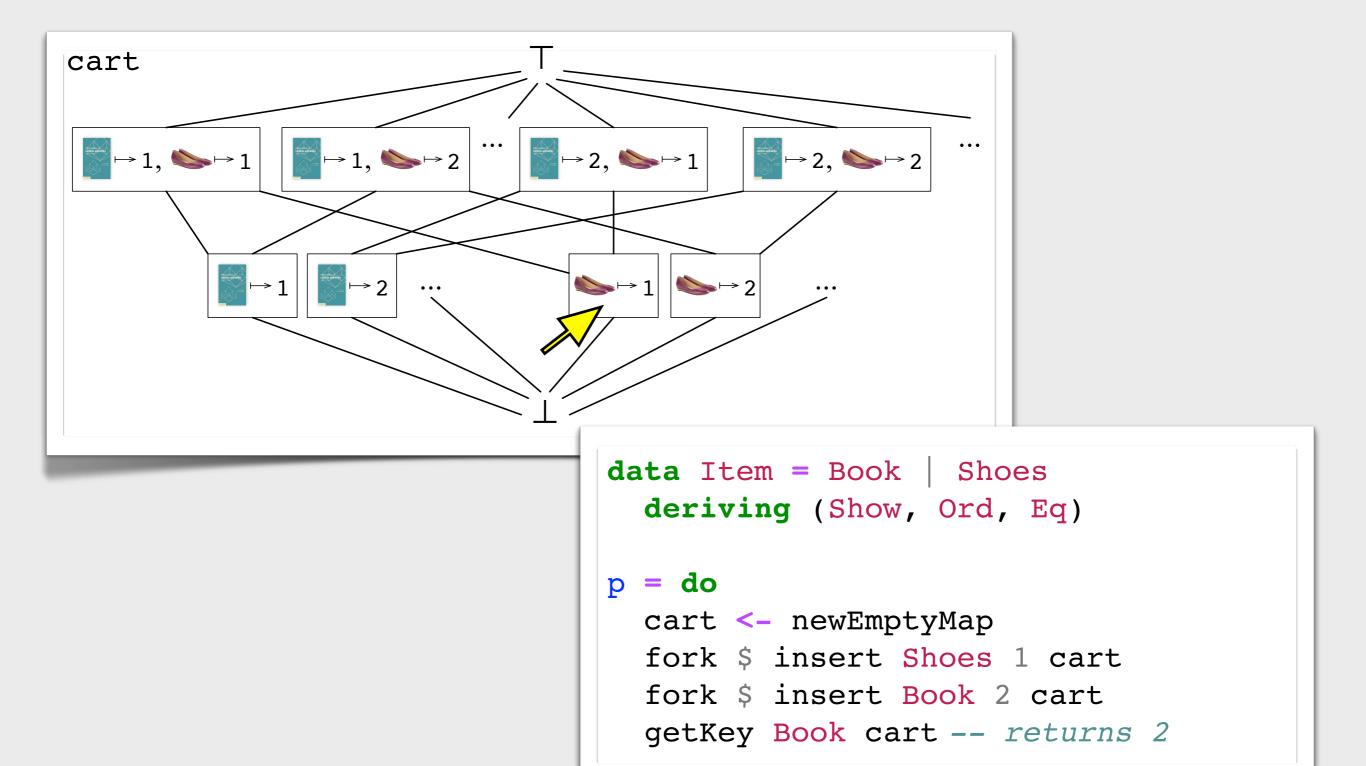
do

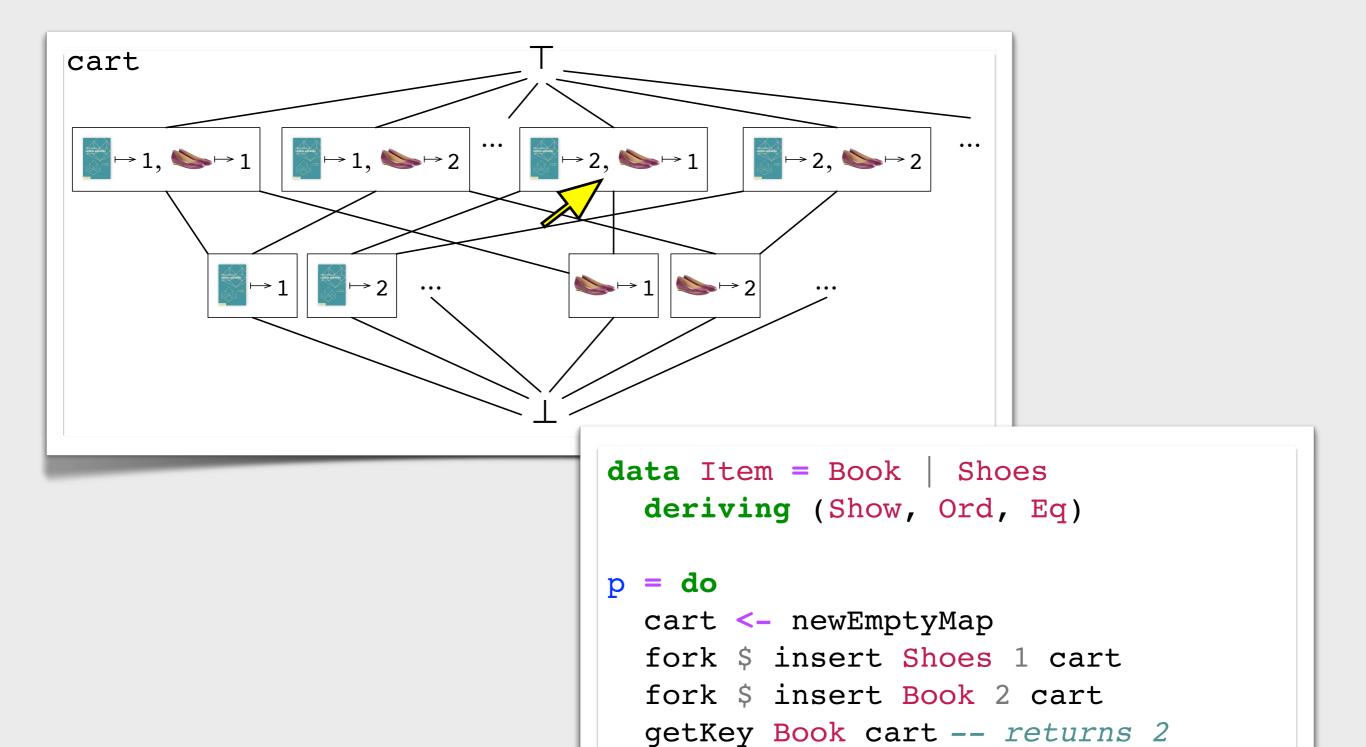
fork (put num 4)

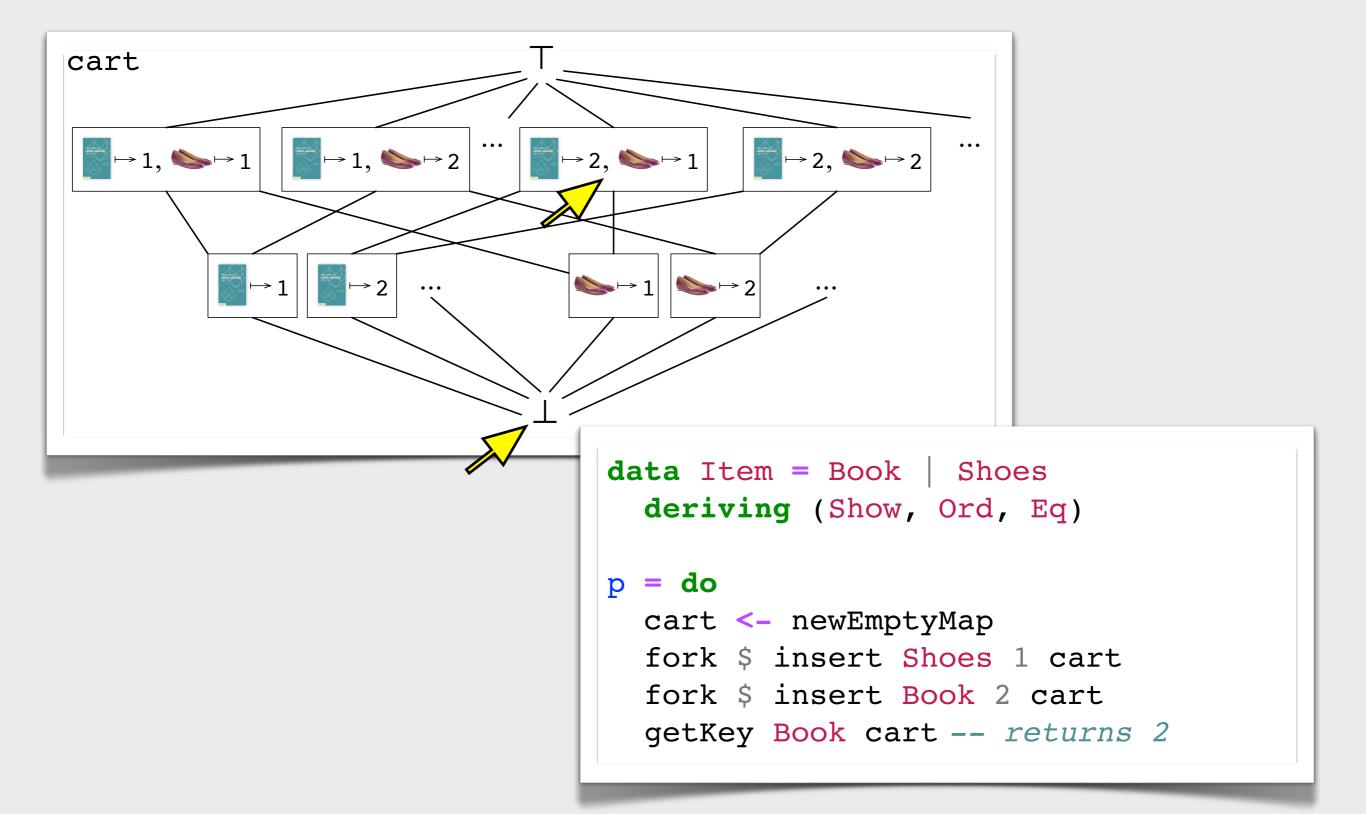
fork (put num 4)
```

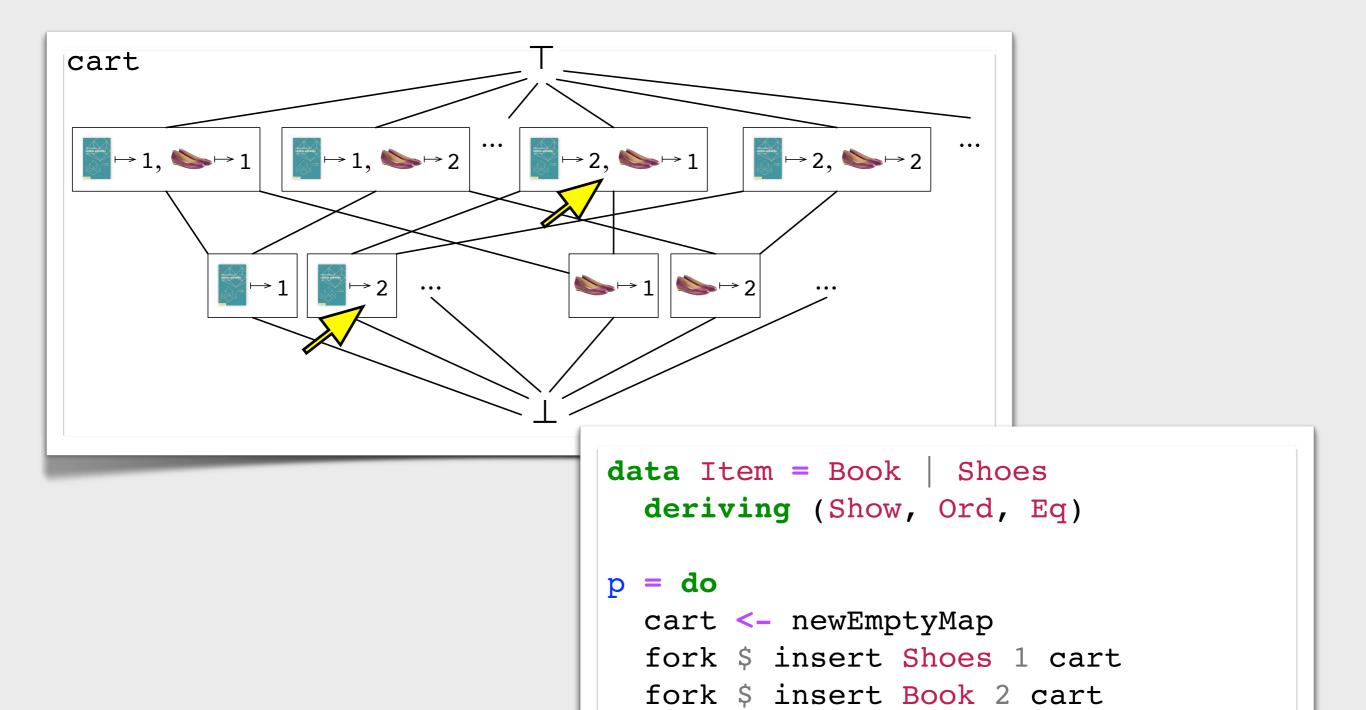




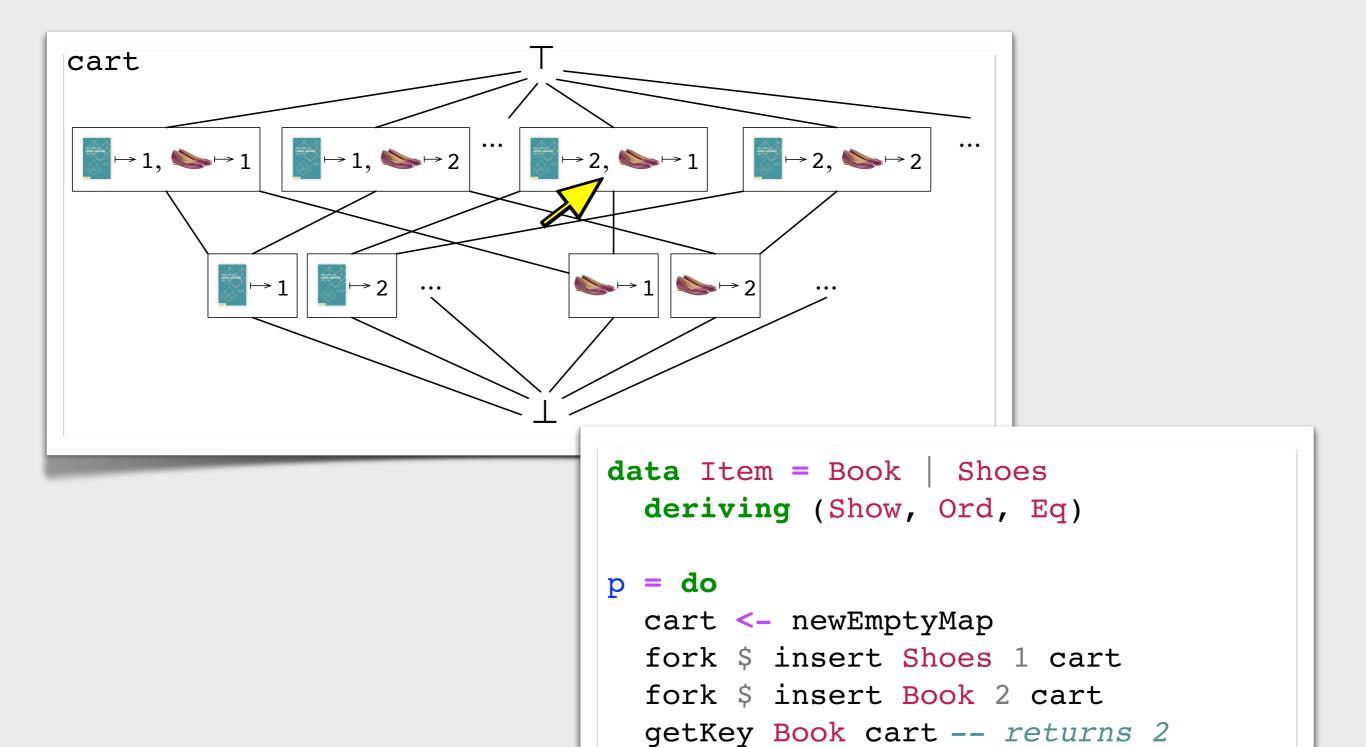


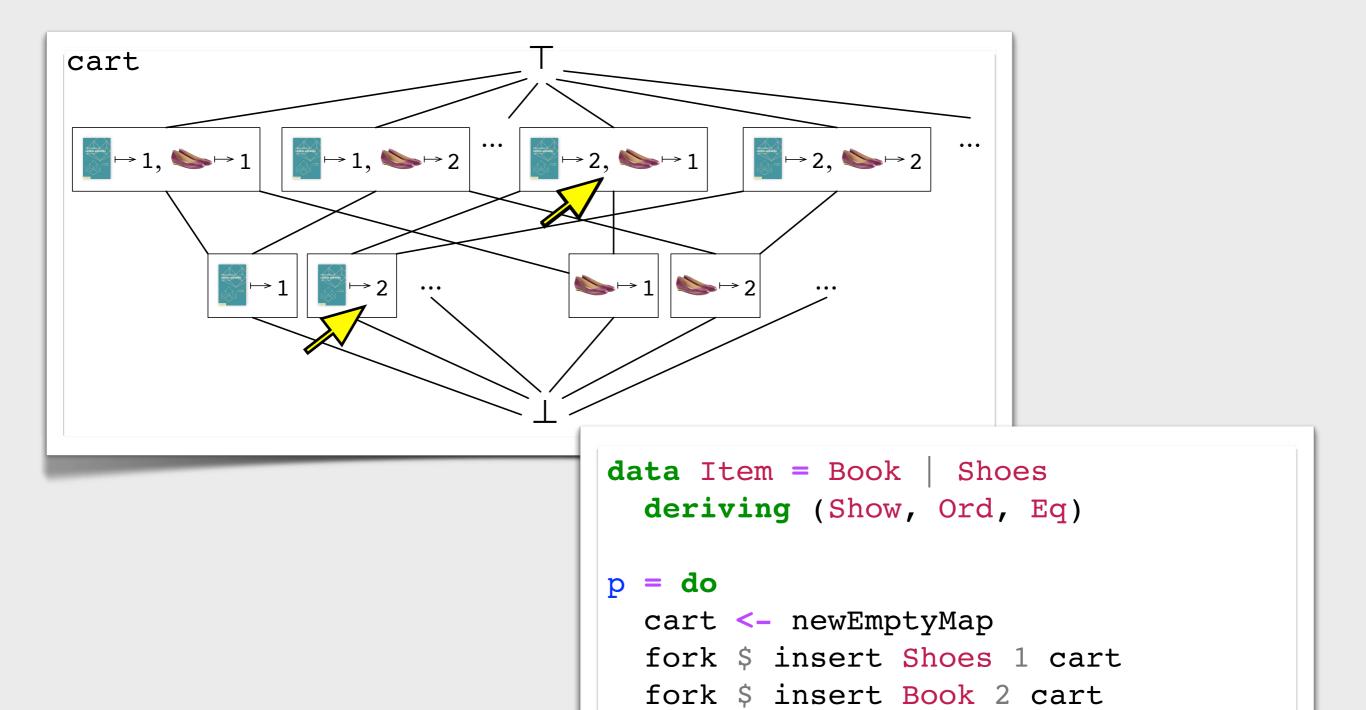




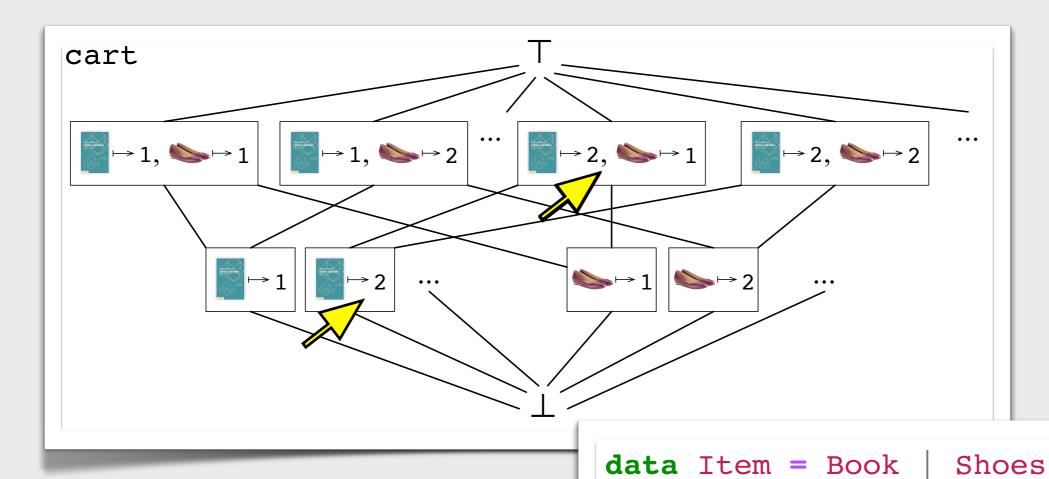


getKey Book cart -- returns 2





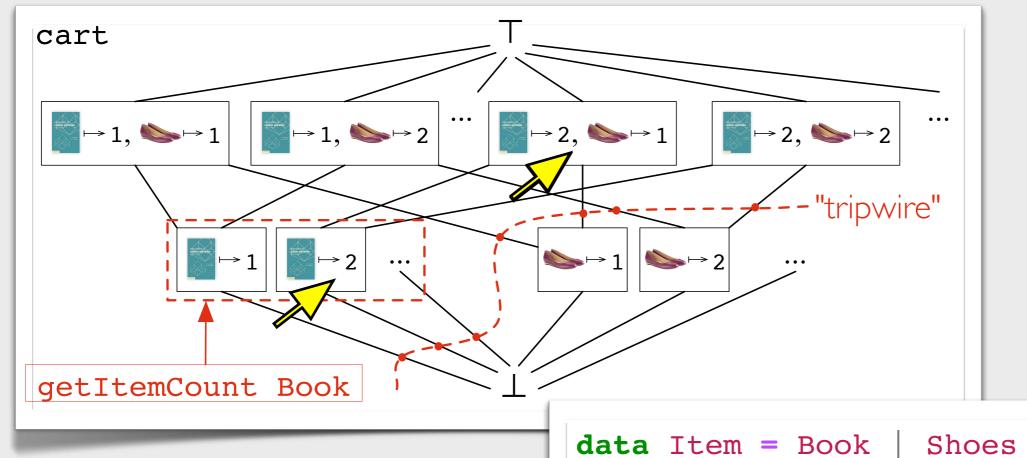
getKey Book cart -- returns 2



```
\{(Book, 1), (Book, 2), ...\}
```

```
deriving (Show, Ord, Eq)

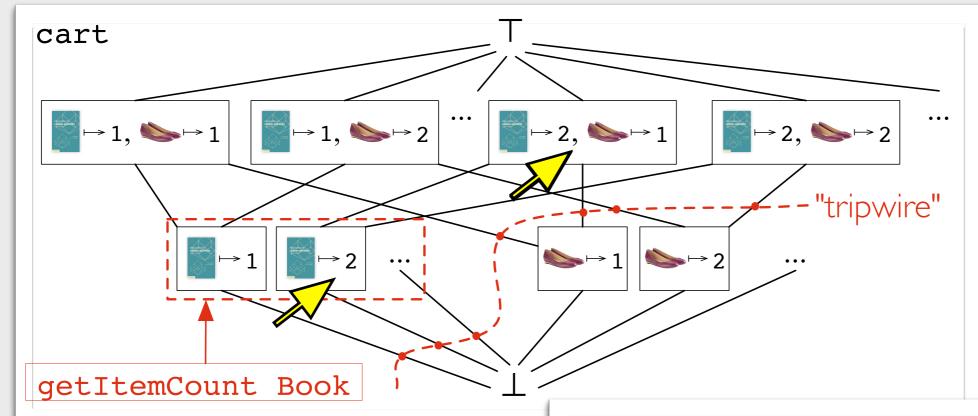
p = do
    cart <- newEmptyMap
    fork $ insert Shoes 1 cart
    fork $ insert Book 2 cart
    getKey Book cart -- returns 2</pre>
```



```
\{(Book, 1), (Book, 2), ...\}
```

```
deriving (Show, Ord, Eq)

p = do
    cart <- newEmptyMap
    fork $ insert Shoes 1 cart
    fork $ insert Book 2 cart
    getKey Book cart -- returns 2</pre>
```

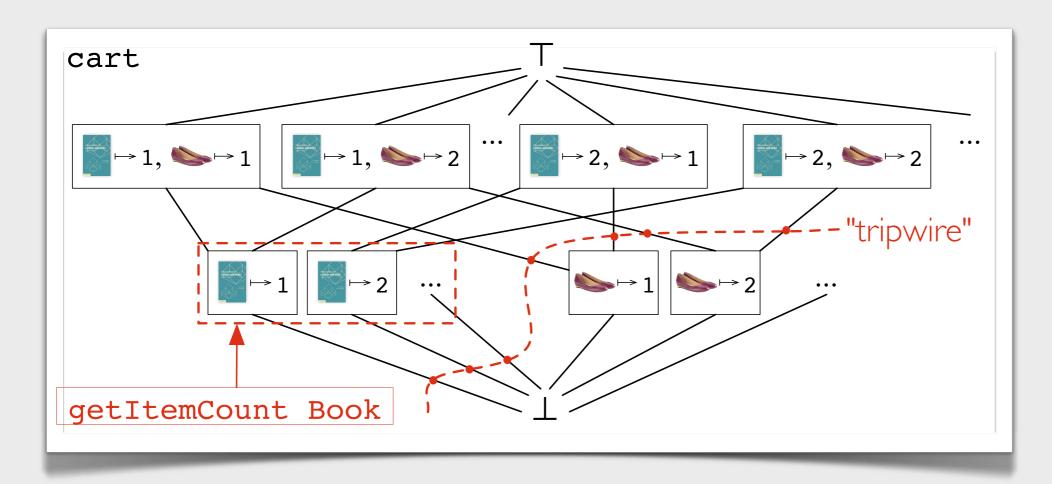


```
\{(Book, 1), (Book, 2), ...\}
```

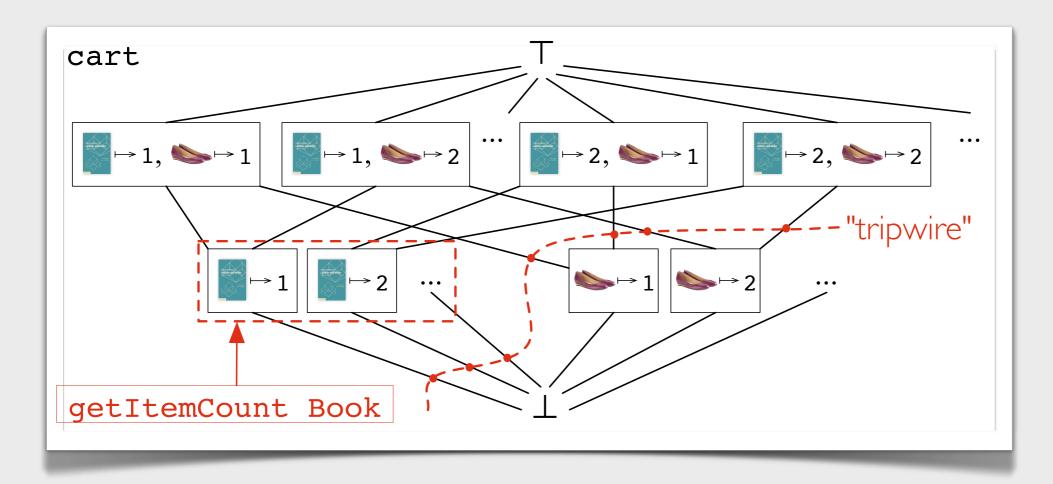
The threshold set must be pairwise incompatible

```
data Item = Book | Shoes
  deriving (Show, Ord, Eq)

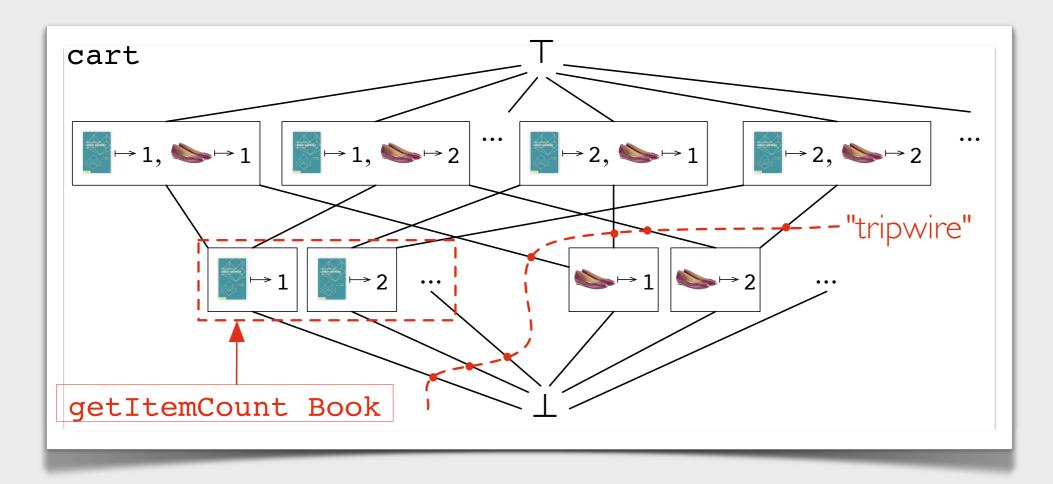
p = do
  cart <- newEmptyMap
  fork $ insert Shoes 1 cart
  fork $ insert Book 2 cart
  getKey Book cart -- returns 2</pre>
```



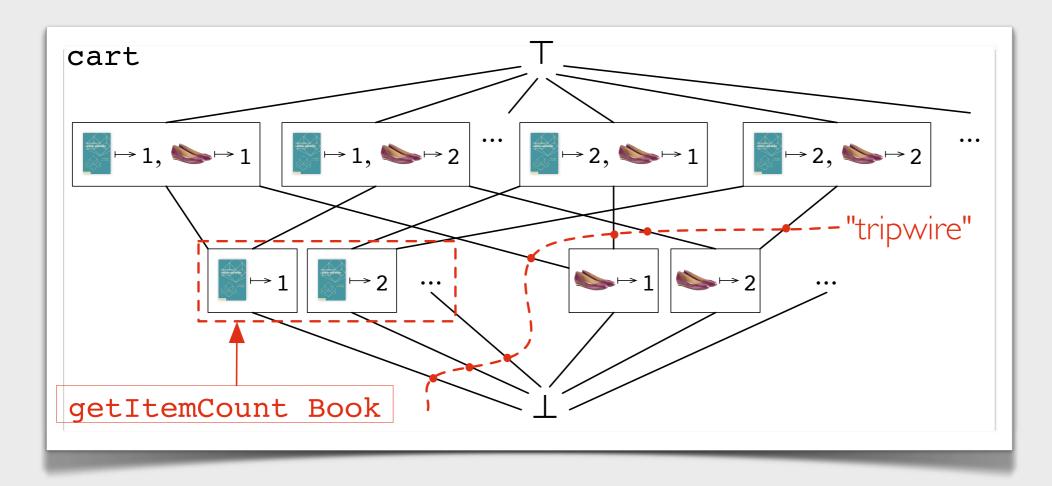
X Can't see the exact, complete contents of the cart



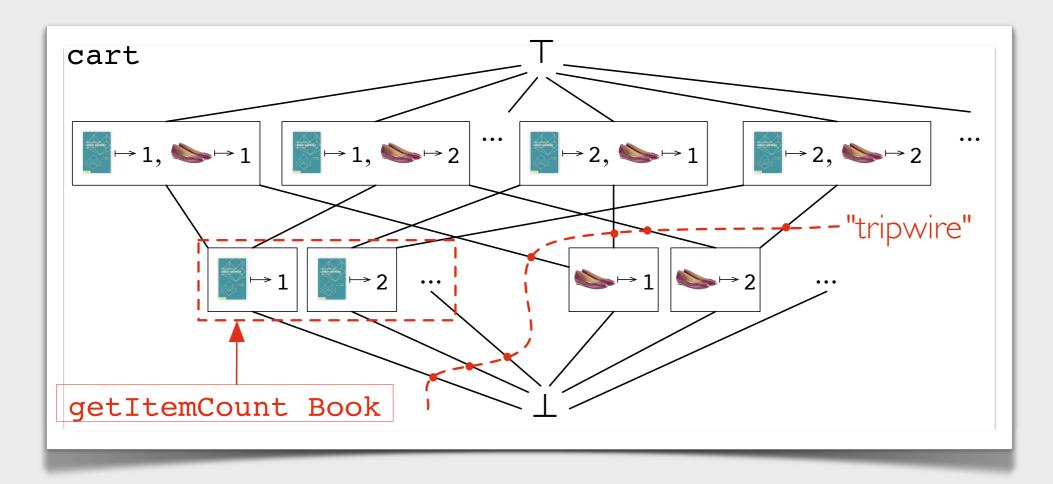
- X Can't see the exact, complete contents of the cart
- X Can't iterate over the items in the cart



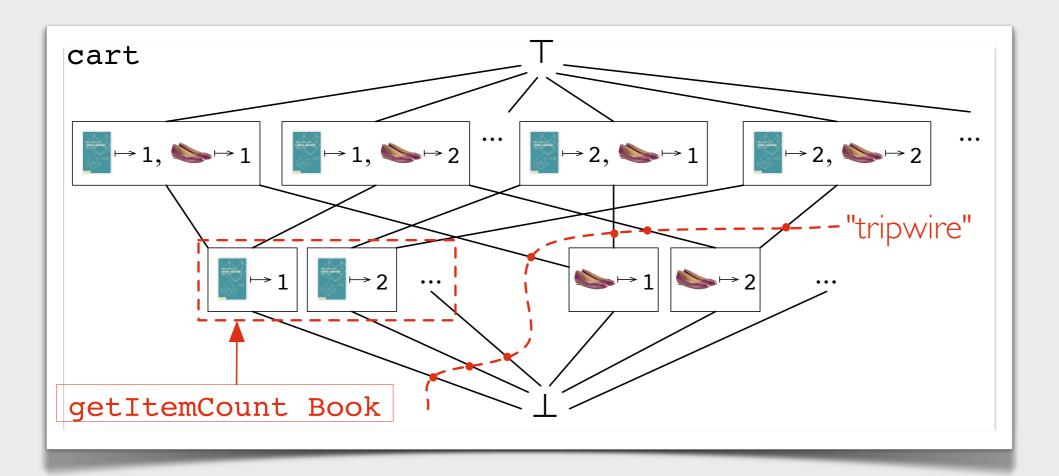
- X Can't see the exact, complete contents of the cart
- X Can't iterate over the items in the cart
- X Can't determine if an item isn't in the cart



- X Can't see the exact, complete contents of the cart
- X Can't iterate over the items in the cart
- X Can't determine if an item isn't in the cart
- X Can't react to writes that we weren't expecting



- Can see the exact, complete contents of the cart
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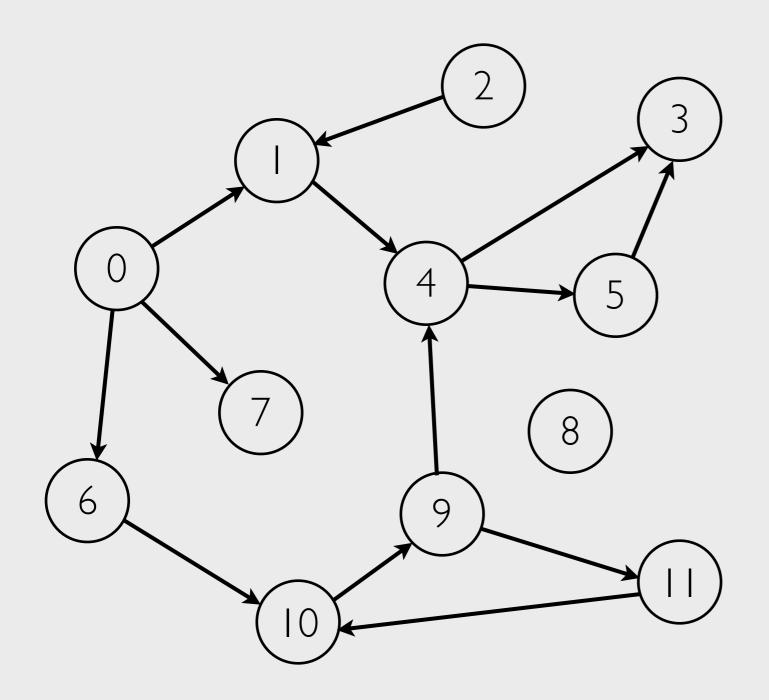


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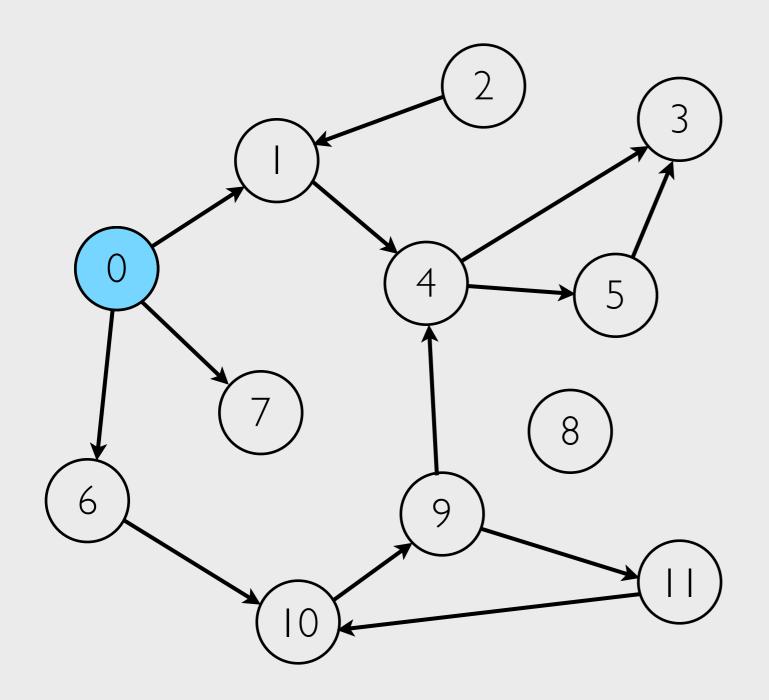
handlers, quiescence, freezing

Freeze After Writing

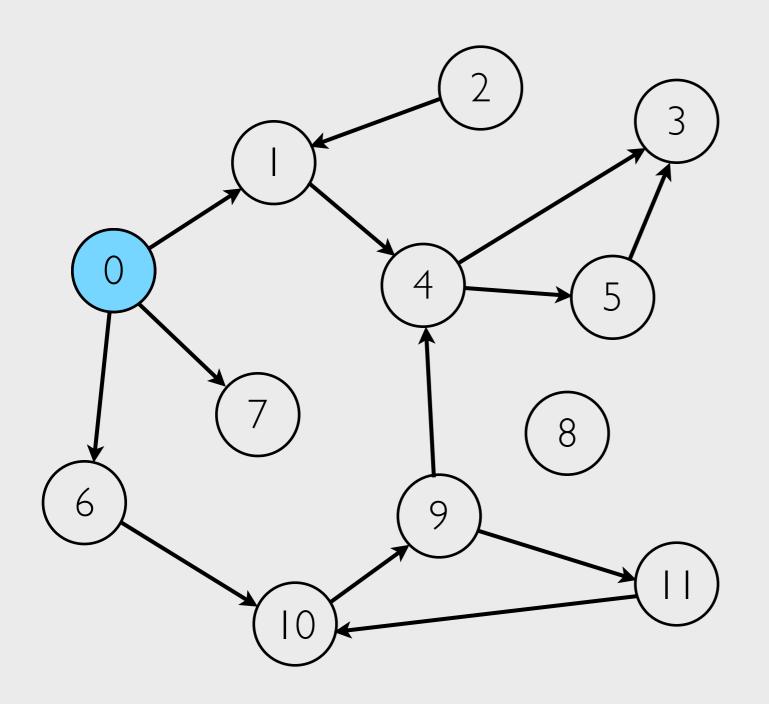
- **LVars**
- Quasi-Deterministic
 Parallel Programming

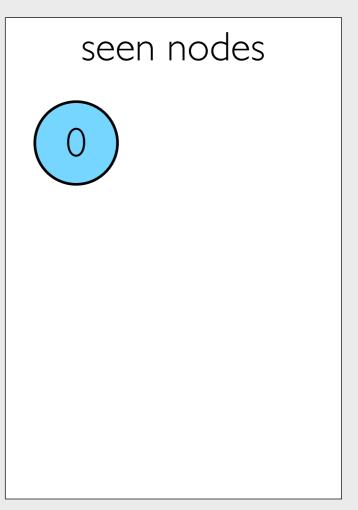


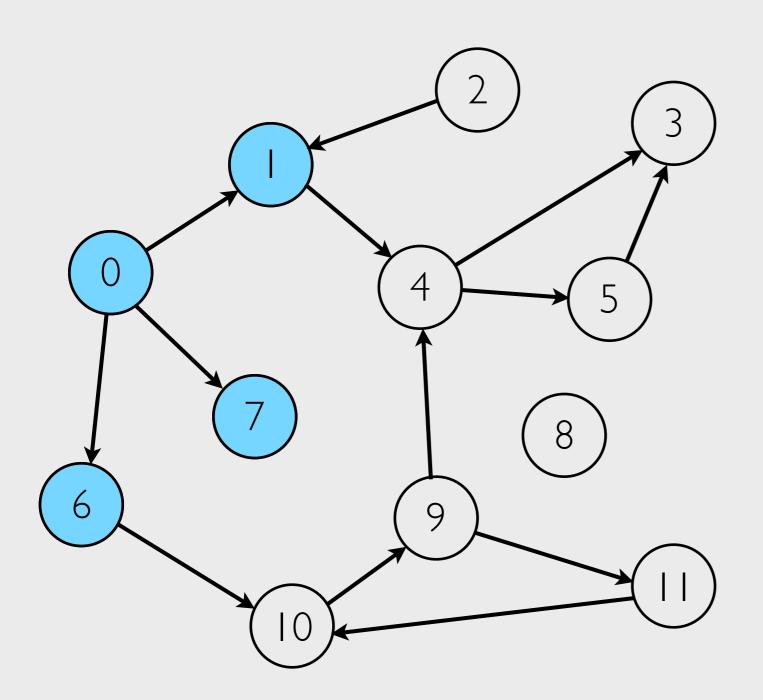
seen nodes

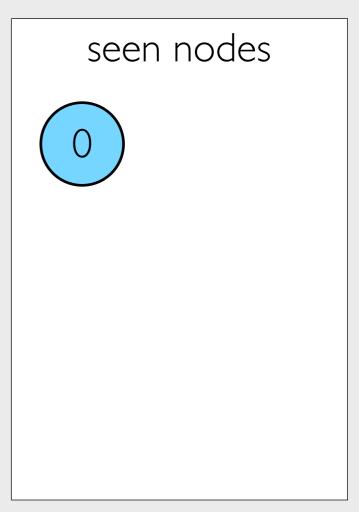


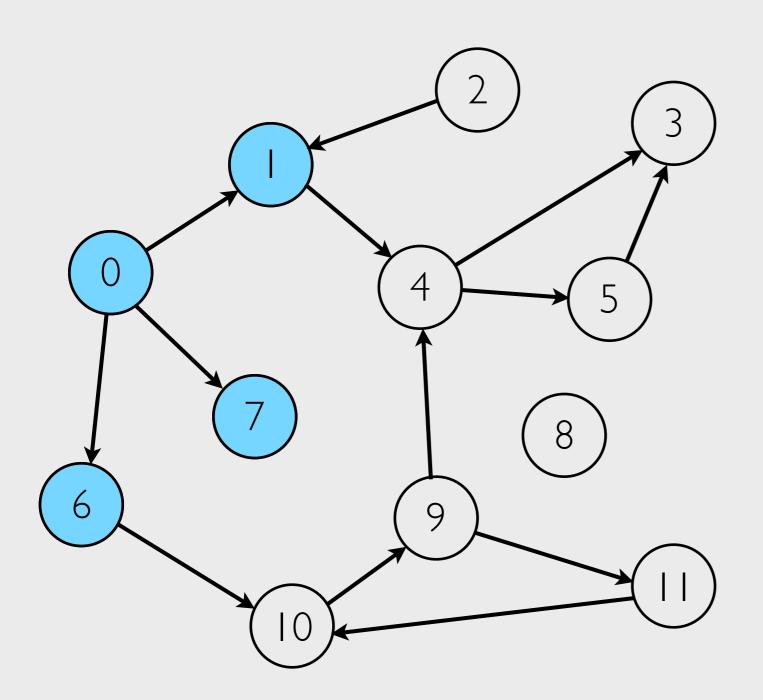
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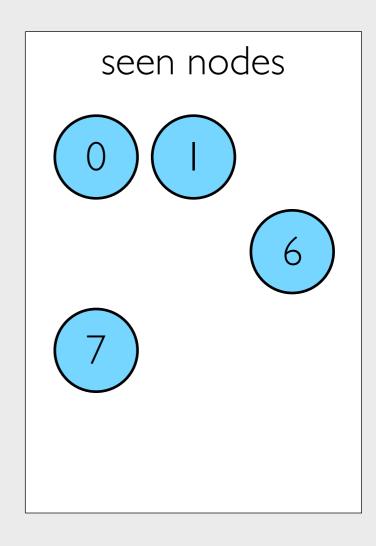


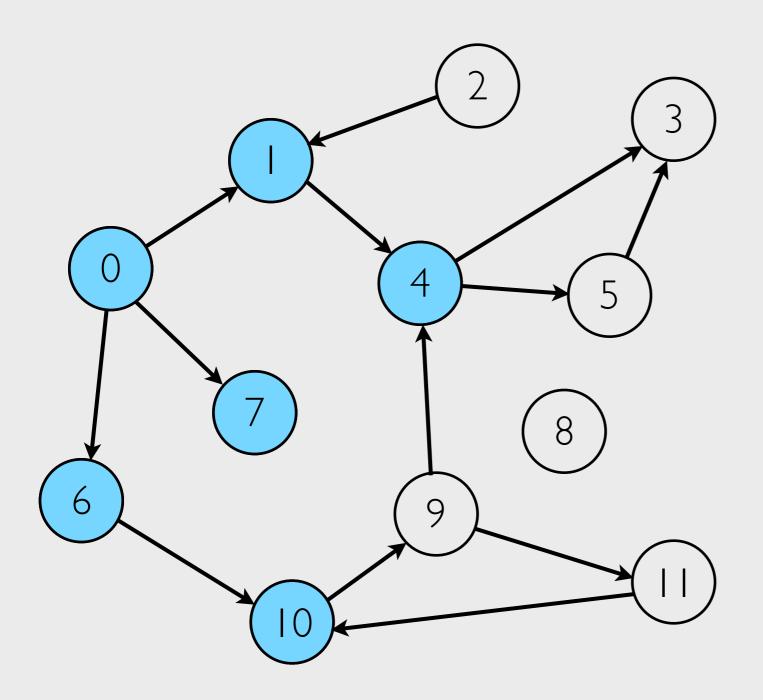


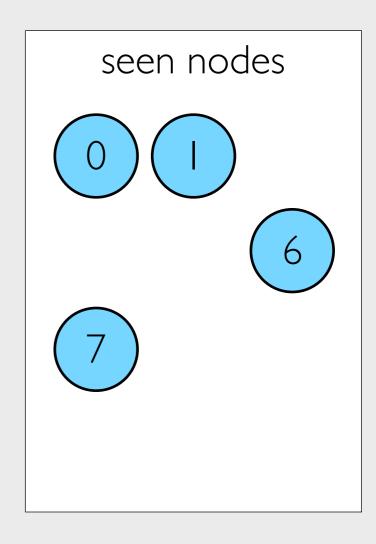


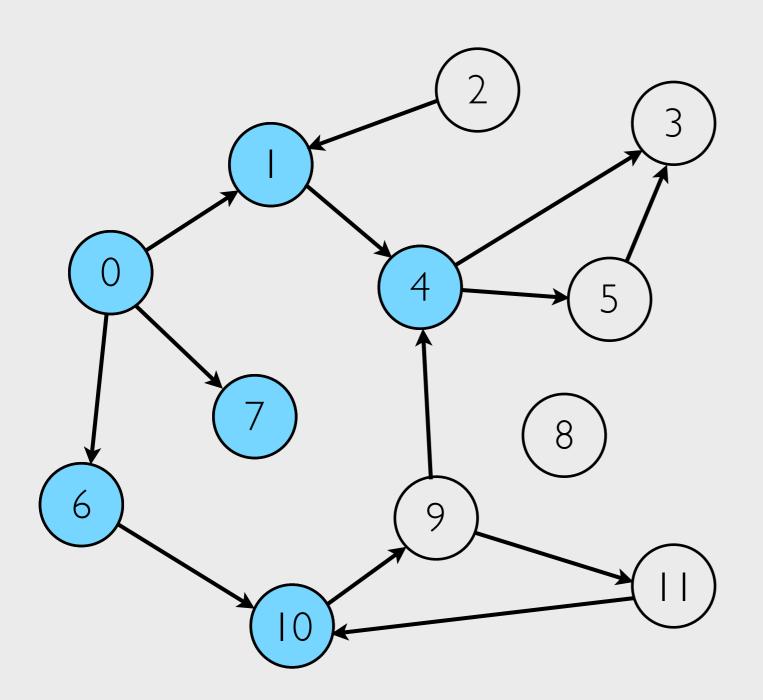


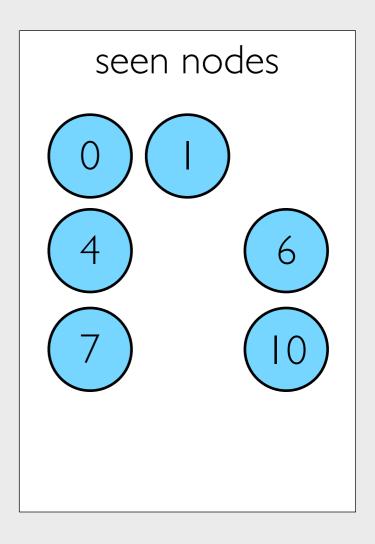


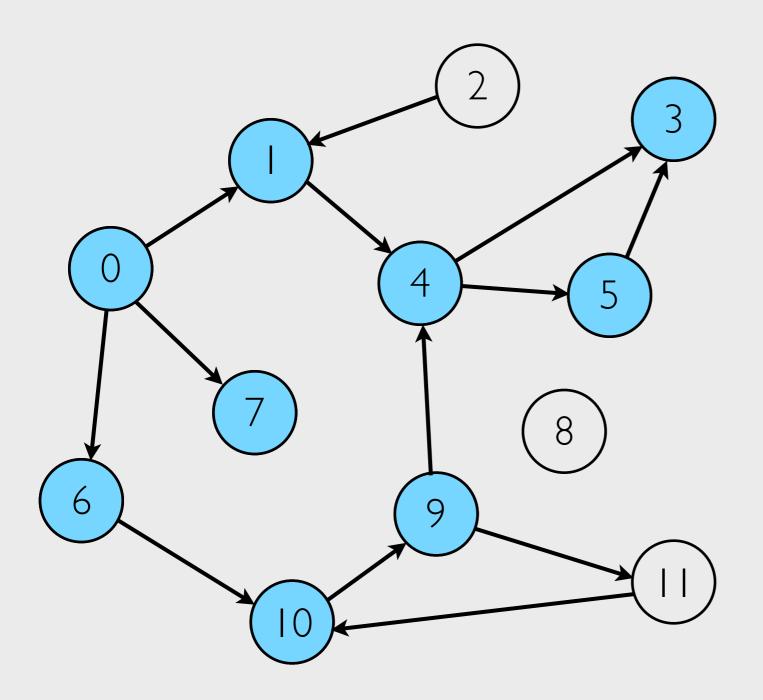


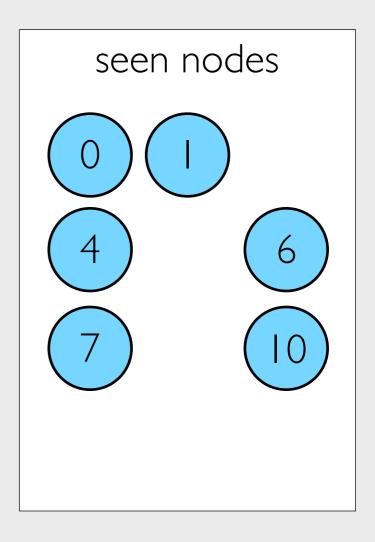


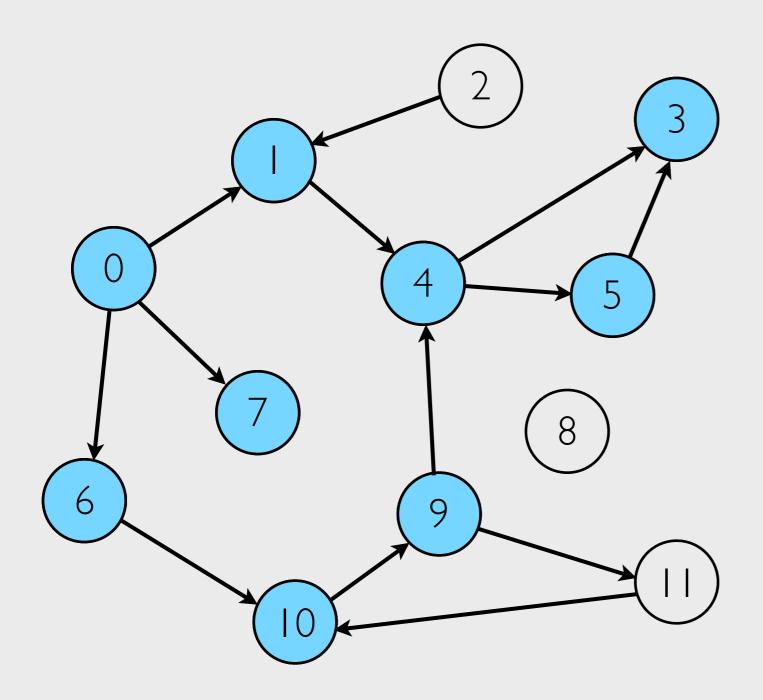


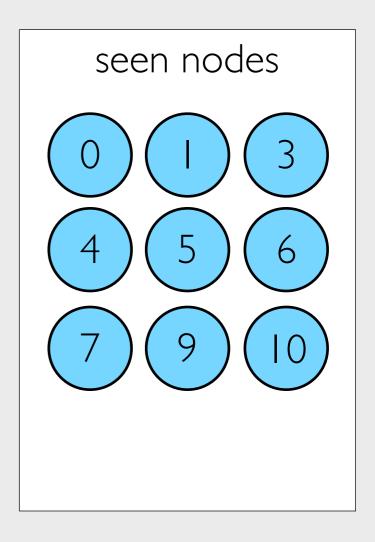


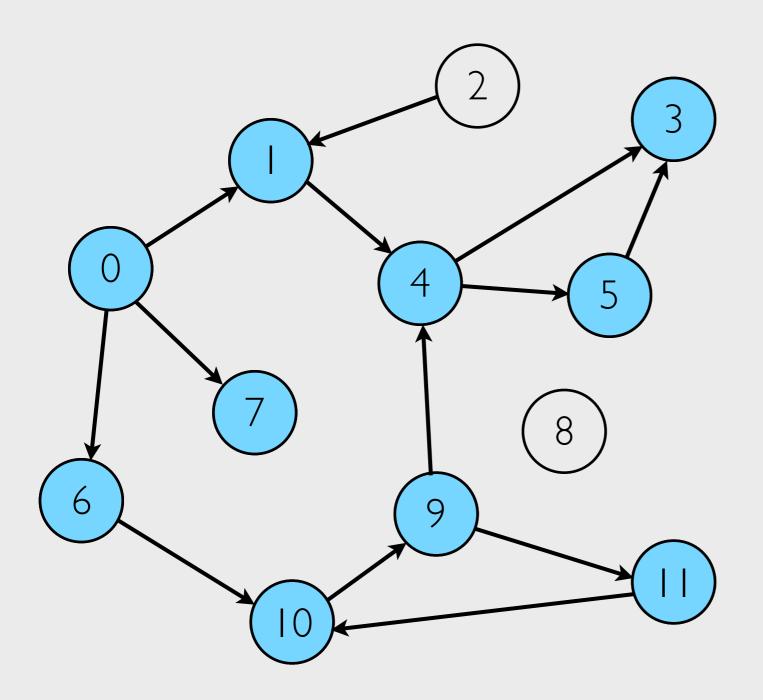


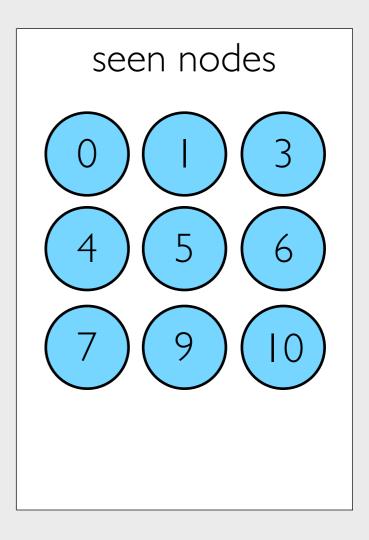


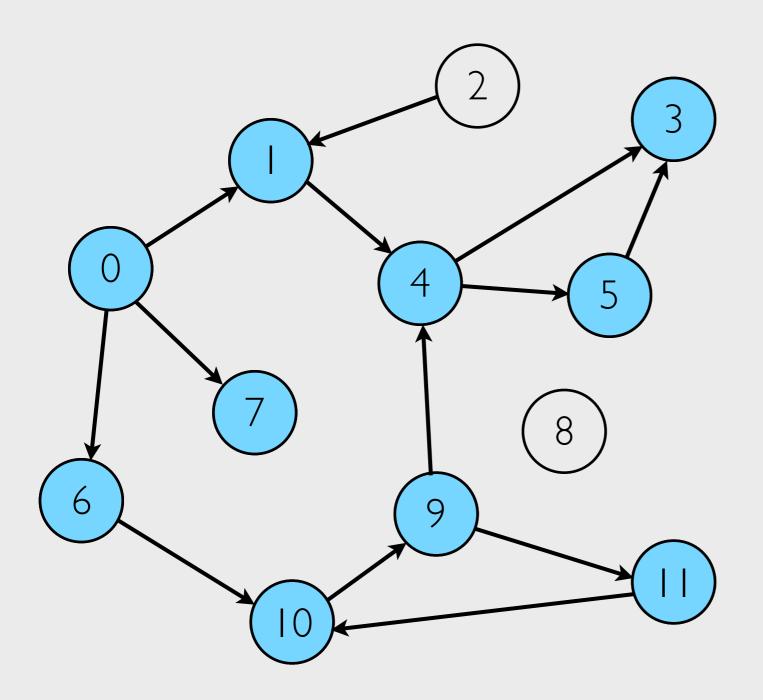


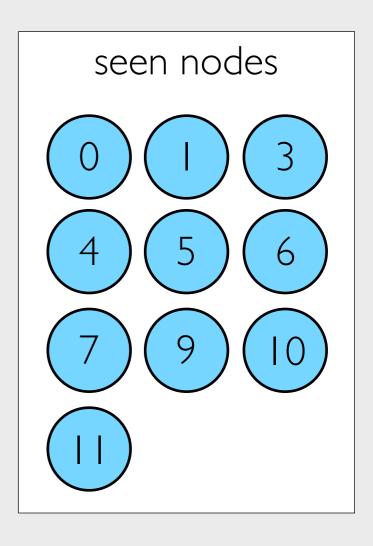


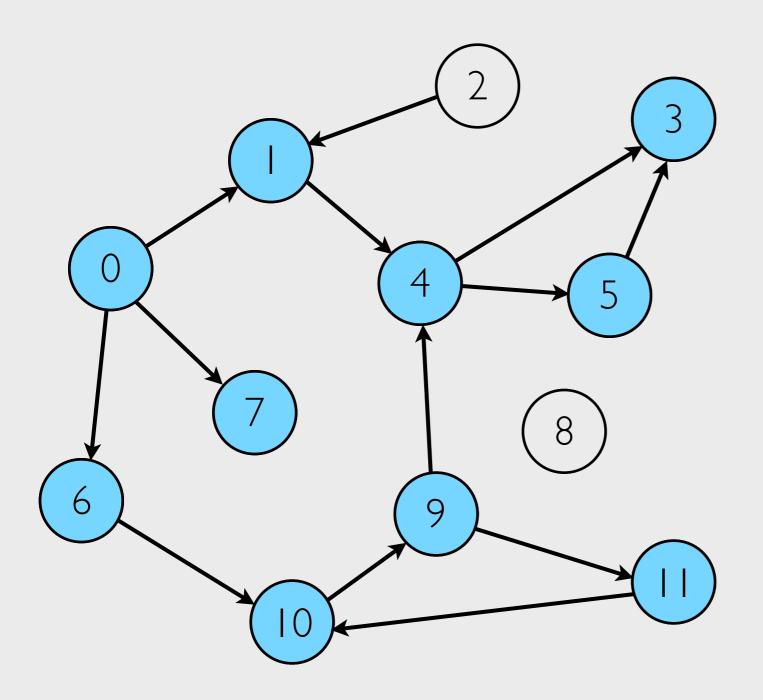


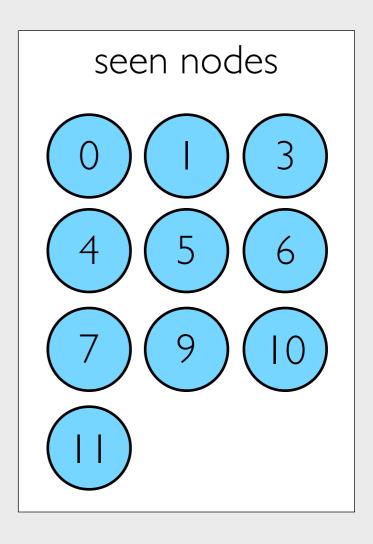


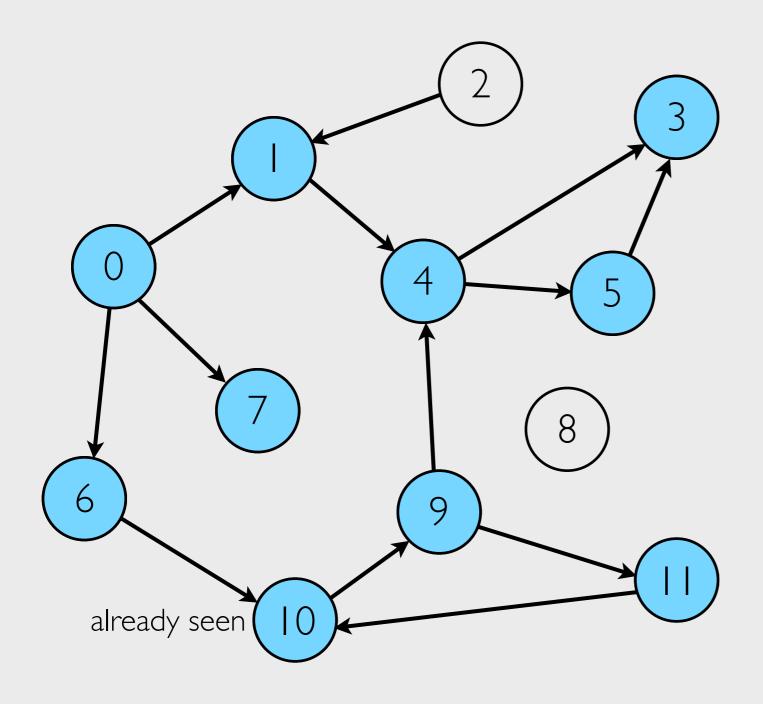


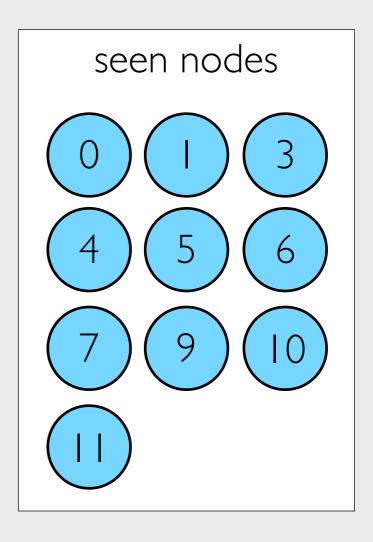


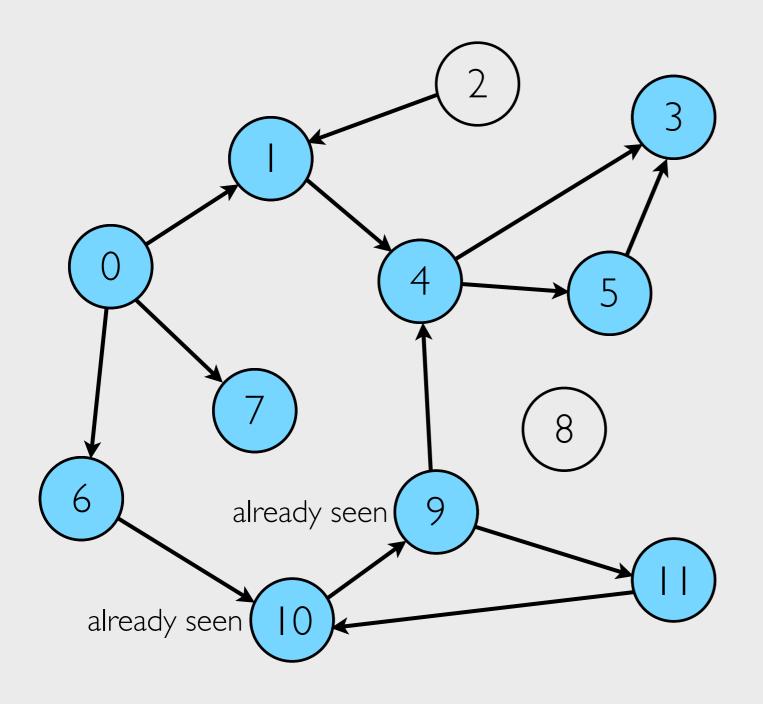


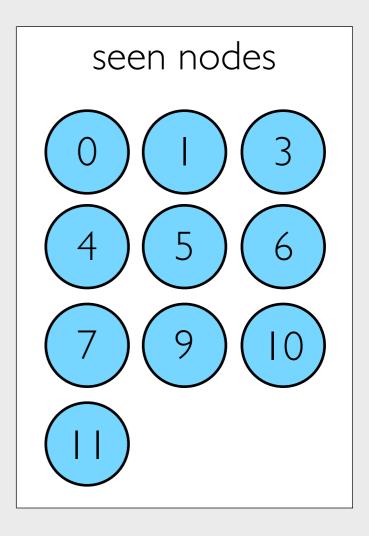


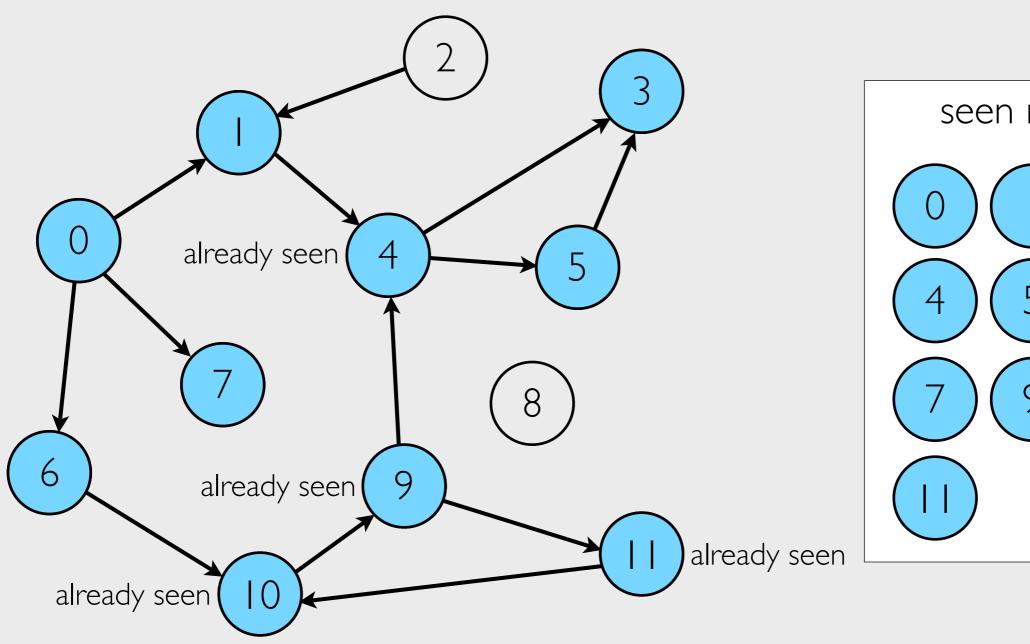


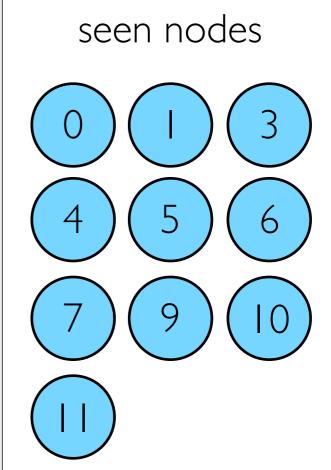


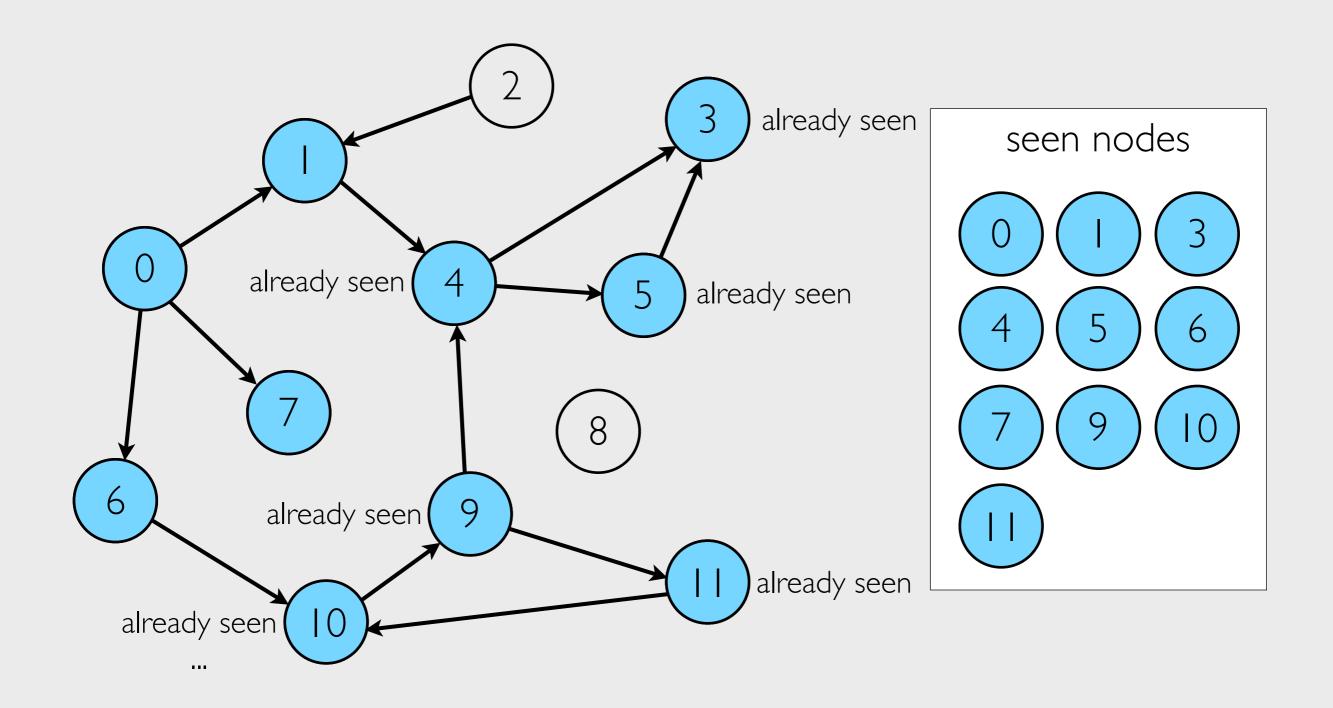


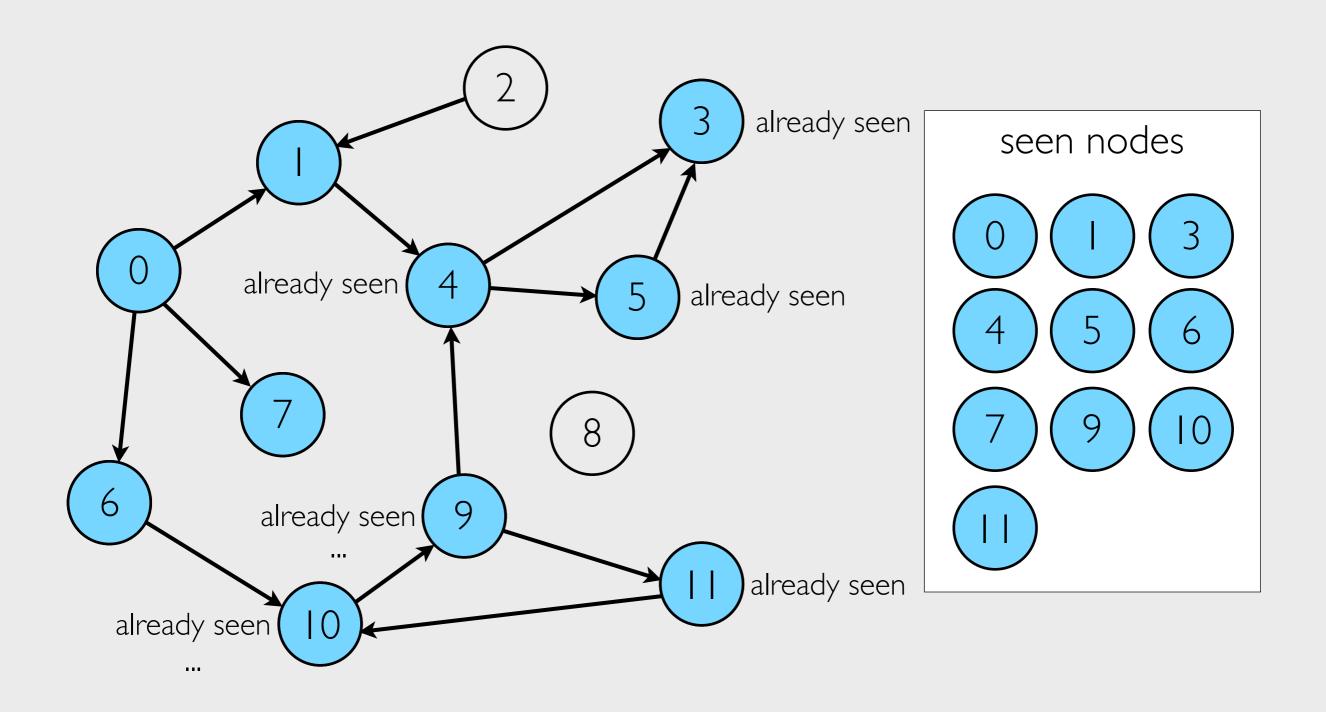


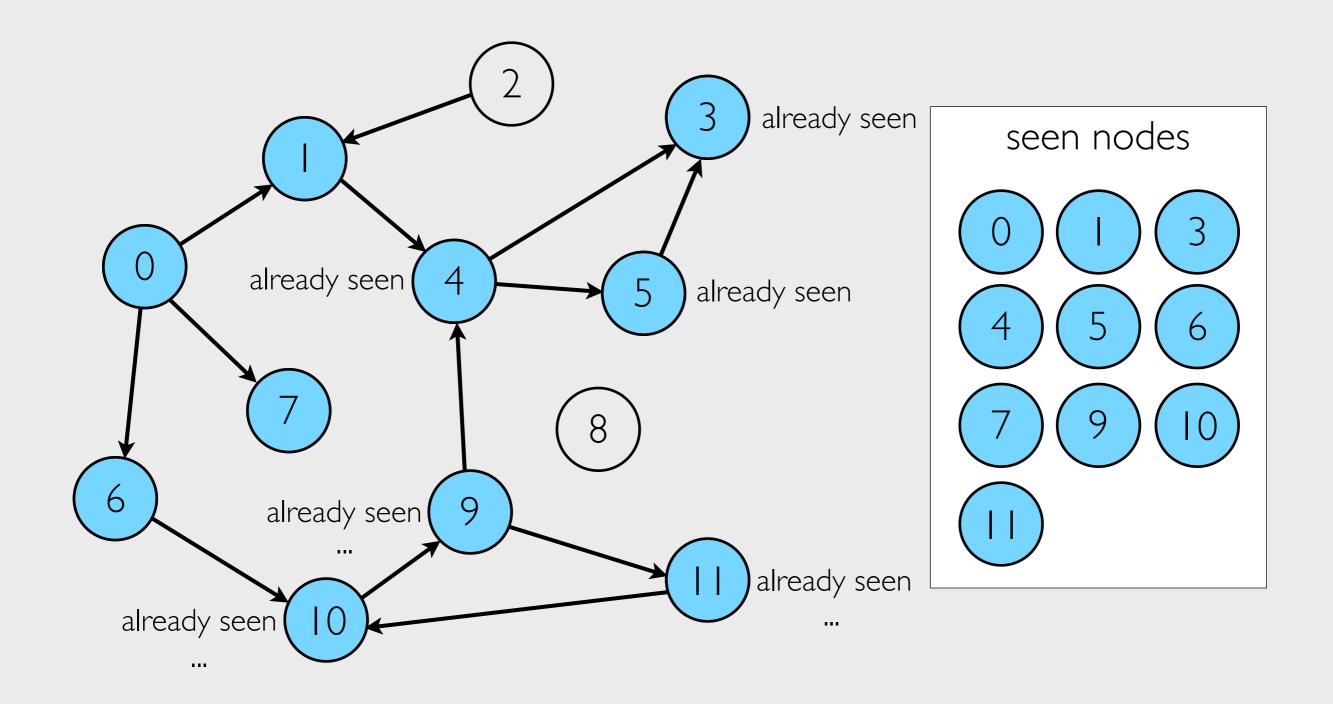




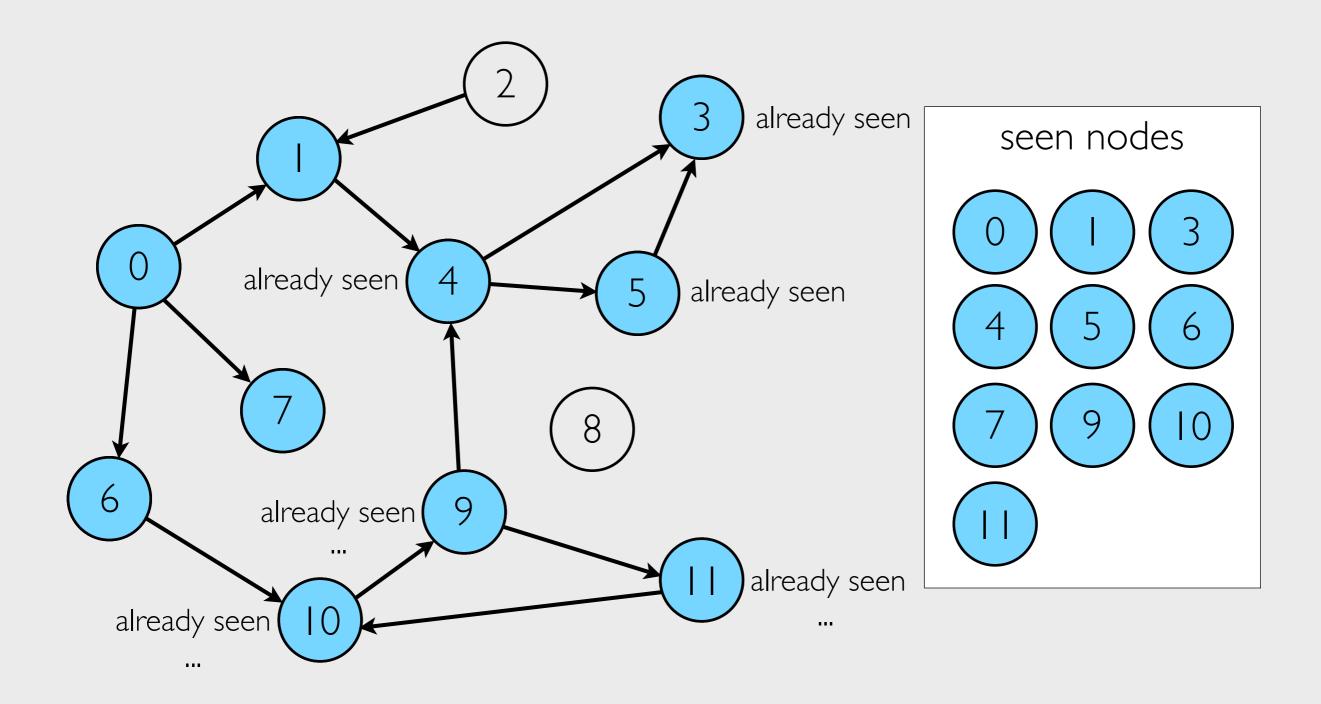




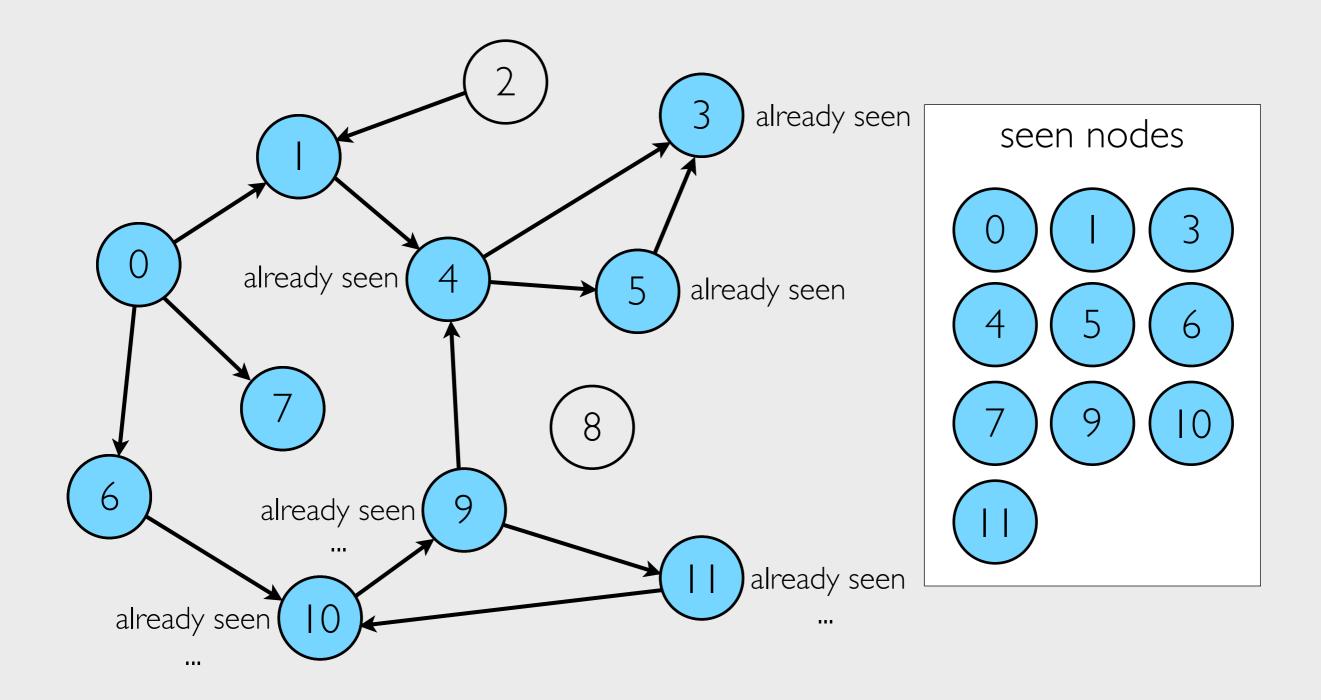


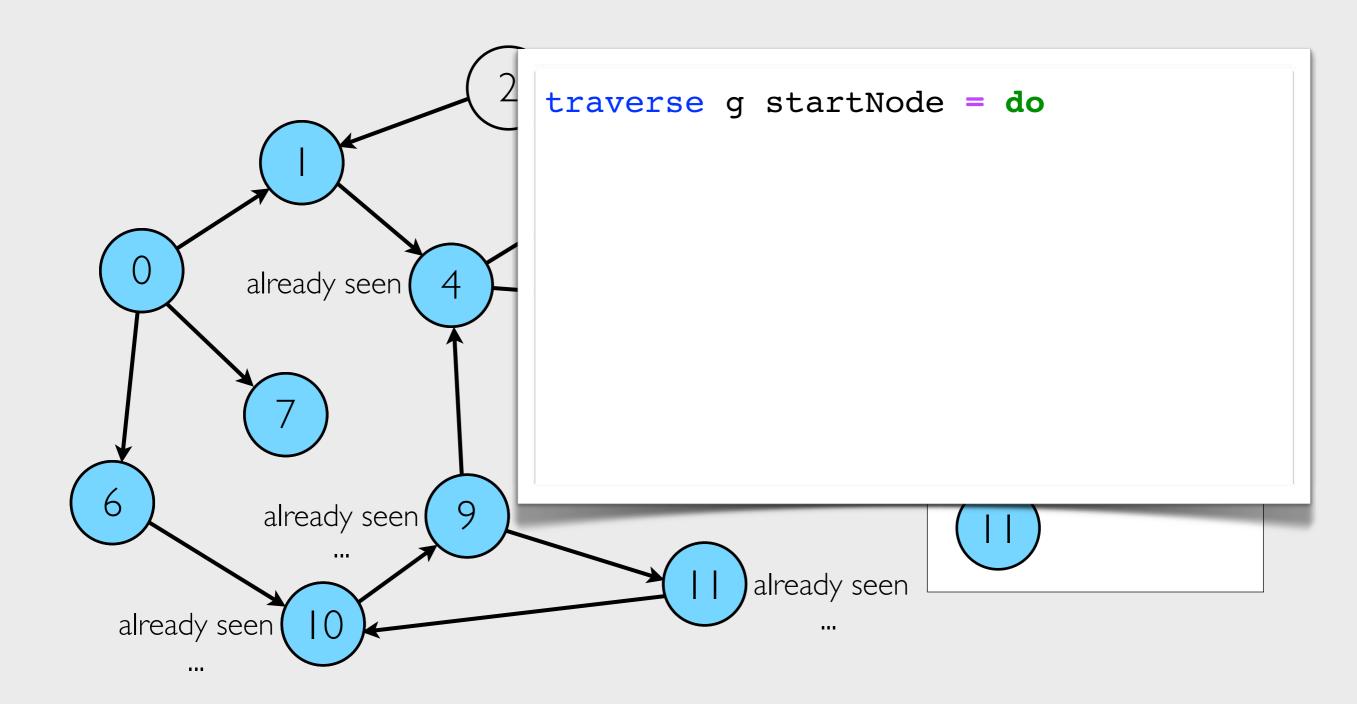


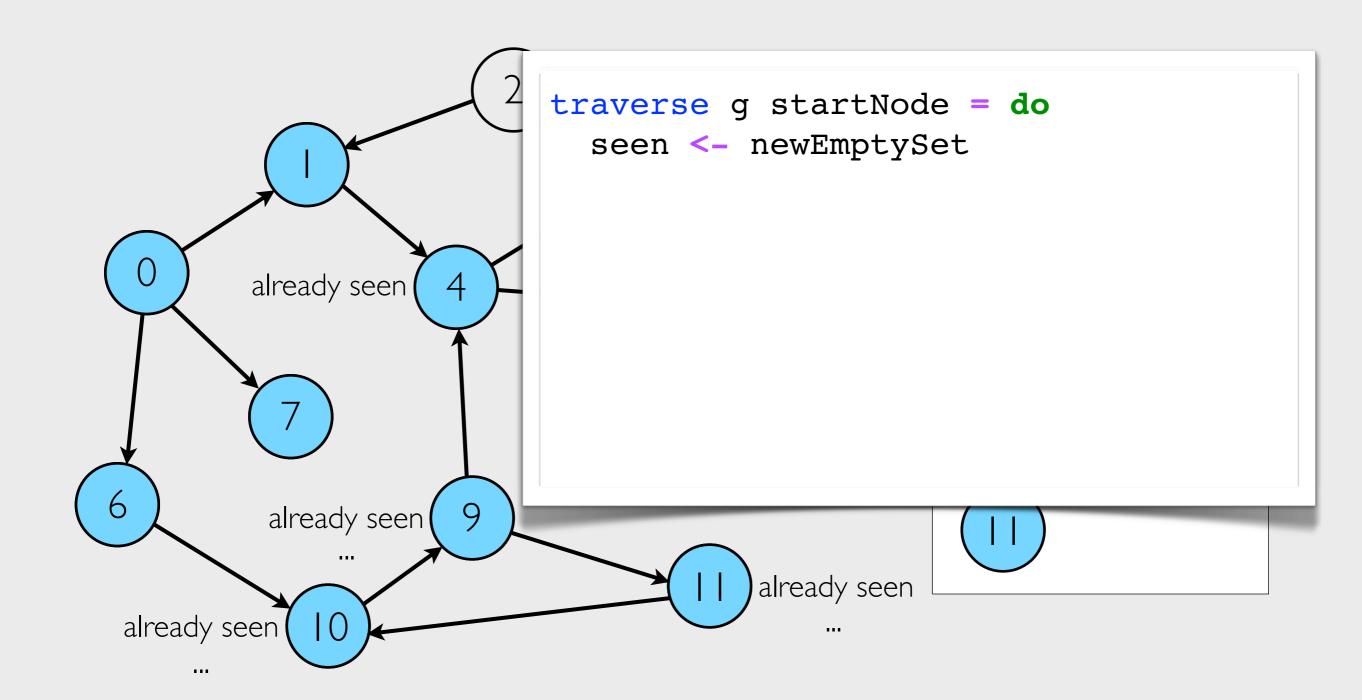
Events are updates that change an LVar's state

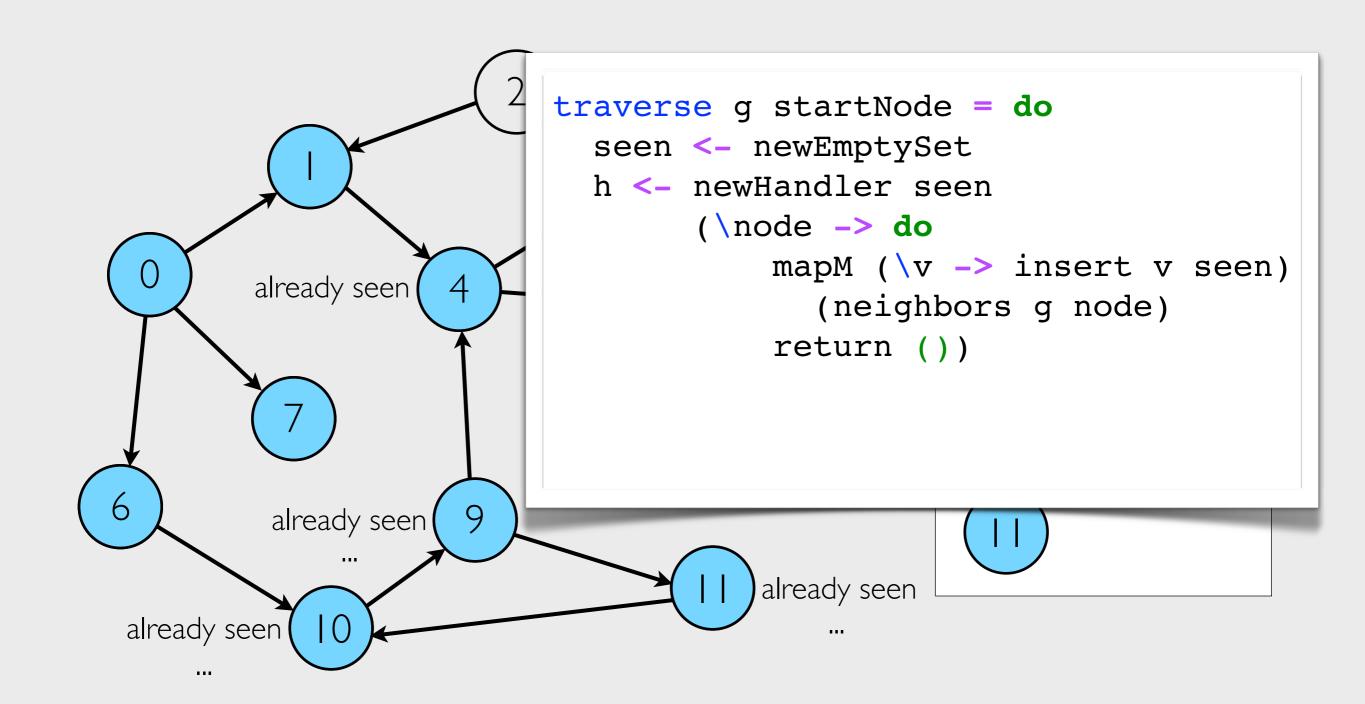


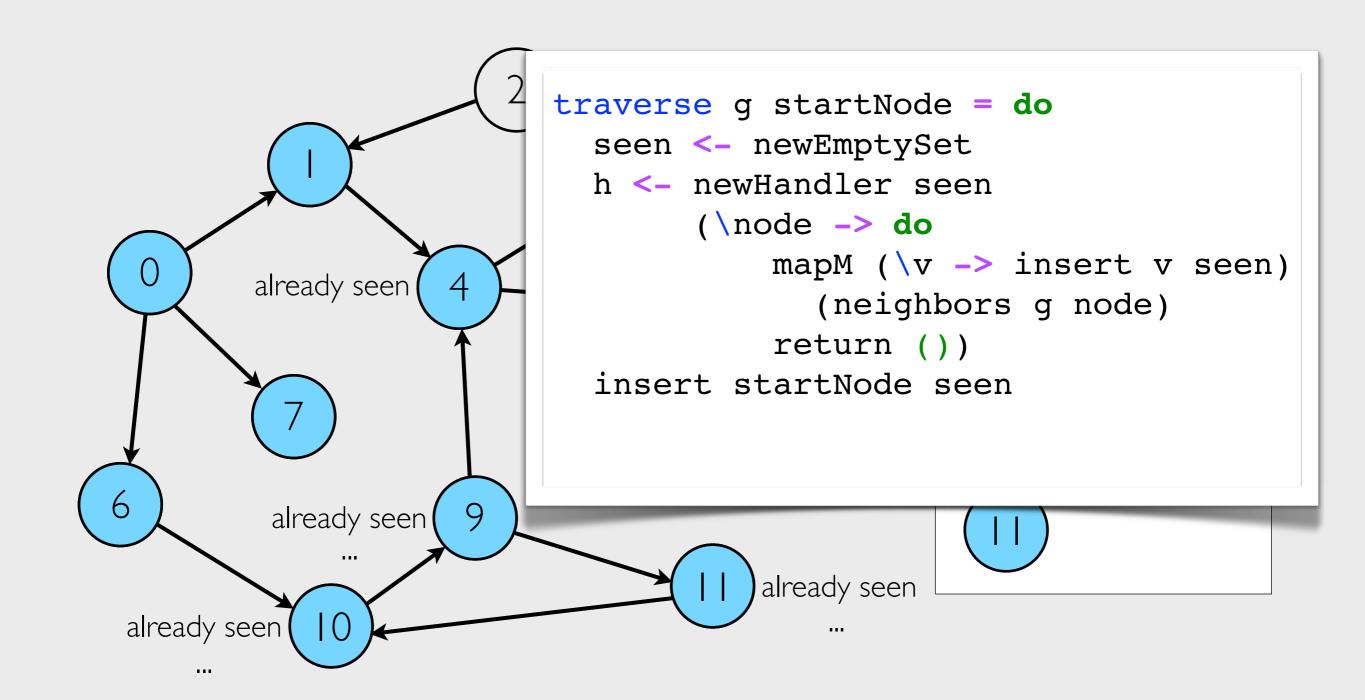
Events are updates that change an LVar's state Event handlers listen for events and launch callbacks in response







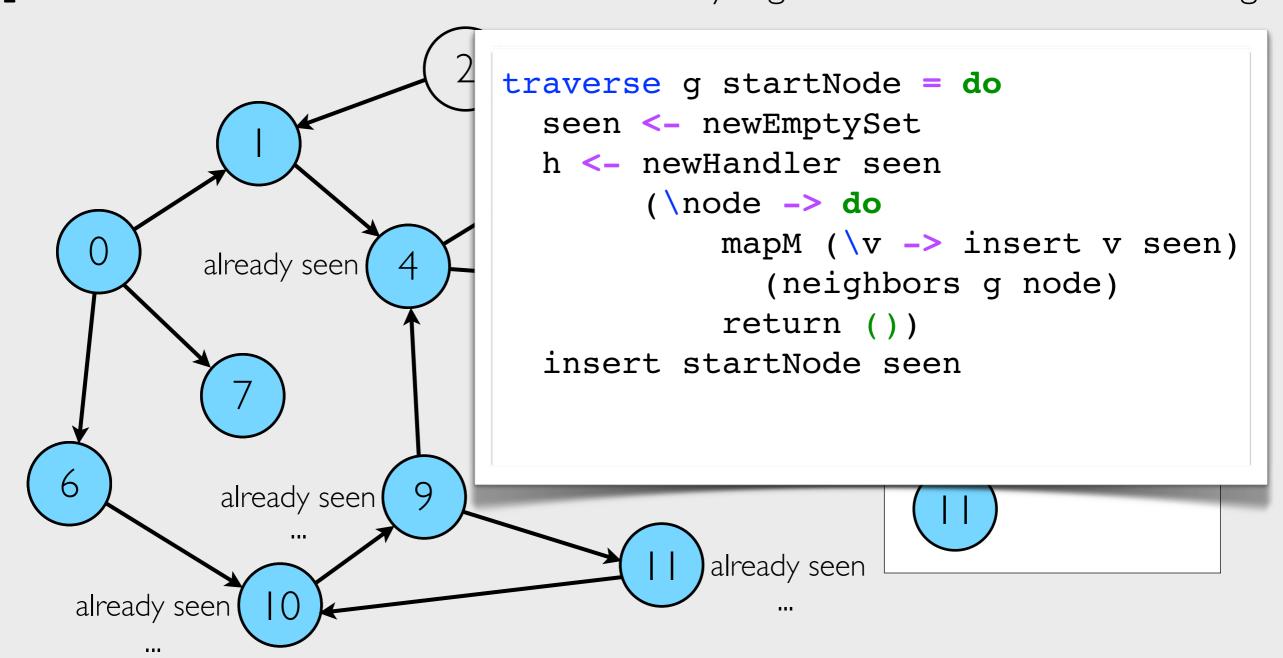




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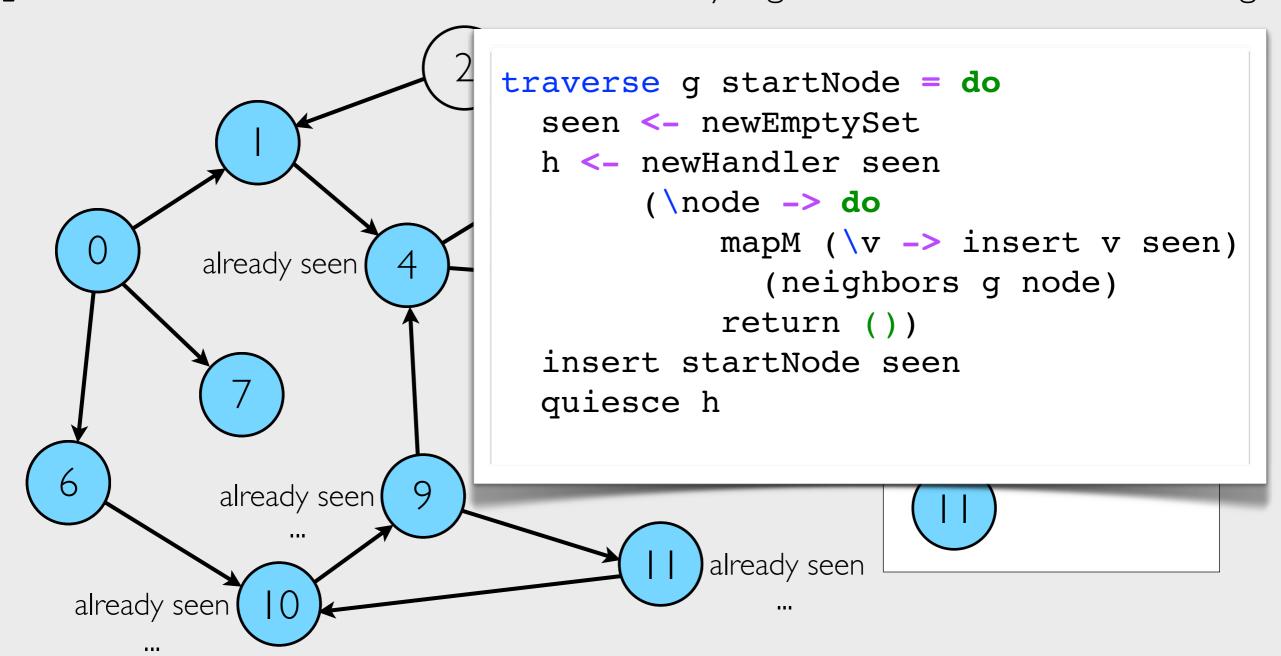
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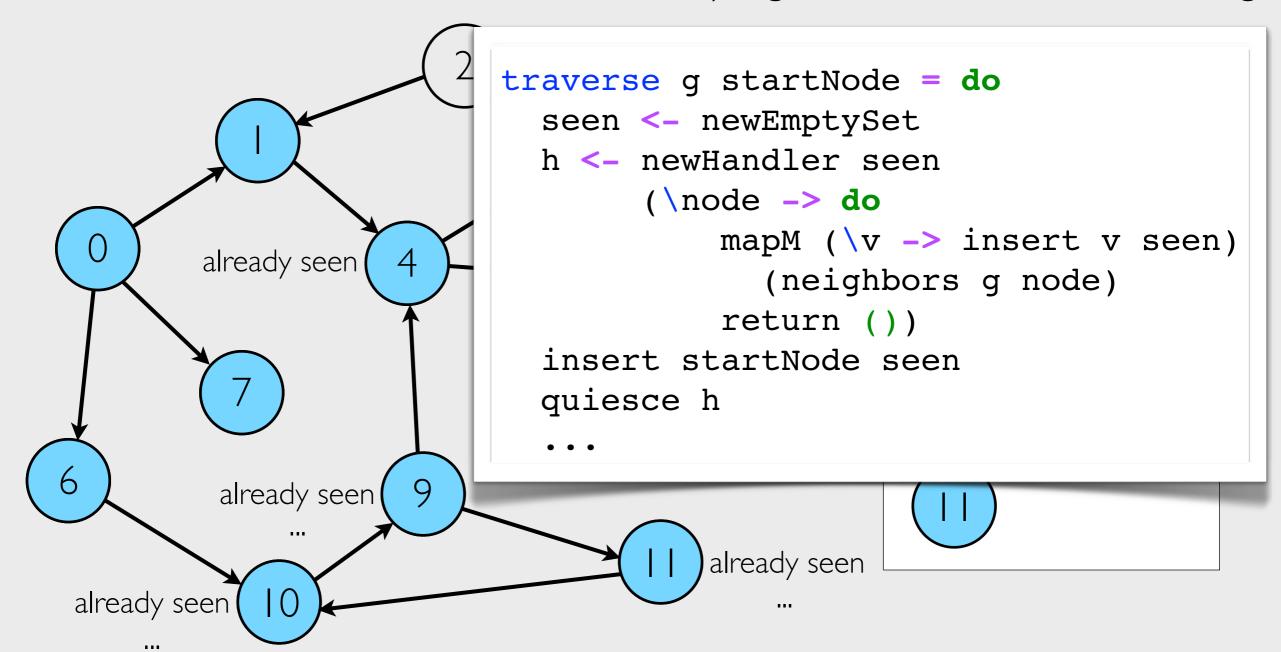
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Attempts to write to a frozen LVar raise a write-after-freeze exception

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Attempts to write to a frozen LVar raise a write-after-freeze exception Two possible outcomes: either the same final value or an exception

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```
Theorem 1 (Quasi-Determinism). If \sigma \hookrightarrow^* \sigma' and \sigma \hookrightarrow^* \sigma'', 10 and neither \sigma' nor \sigma'' can take a step, then either:
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- 1. $\sigma' = \sigma''$ up to a permutation on locations π , or
- 2. $\sigma' = \operatorname{error} \operatorname{or} \sigma'' = \operatorname{error}$.

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```
[(Book,1),(Shoes,1)]
```

[(Book,1)]

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[(Book,1),(Shoes,1)] or error.



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a Haskell library for programming with LVars



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a Haskell library for programming with LVars

L'Var operations run inside a **Par** monad Library-level threads

Par computations indexed by effect level

```
p :: Par Det s (IMap Item s Int)
p = do
    cart <- newEmptyMap
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a Haskell library for programming with LVars

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Implement your own LVars, too cabal install lvish today!

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More in the paper and TR

More in the paper and TR

The LVish calculus and its quasi-determinism proof

More in the paper and TR

The LVish calculus and its quasi-determinism proof Gory details of the LVish scheduler implementation More in the paper and TR

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Gory details of the LVish scheduler implementation

Case study: parallel k-CFA written with the LVish library

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